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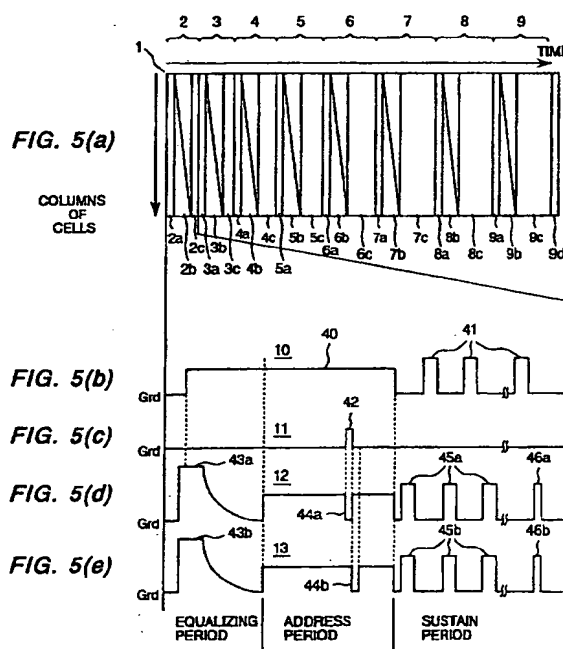
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(54) Plasma display, driving apparatus of plasma display panel and driving system thereof

(57) A plasma display panel driving system having a common X electrode 22 arranged on a front glass substrate 21 driven in common, a independent Y electrode 23 arranged parallel to the common X electrode 22 on the front glass substrate 21 driven independently, an address A electrode arranged perpendicular to the common X electrode and the independent Y electrode on the back glass substrate driven independently, and means for performing at least one electric discharge for equalizing of electric charge particle in a cell in which another electric charge particle is caused beforehand, thereby improving contrast.

Erasing and polarization of electric charge particles are performed by the fine line erasing pulse 46a after sustaining period 2c, and a equalizing pulse having a high voltage is supplied to the independent Y electrode to which the last fine line erasing pulse 46a or 86a is supplied, and a regulating pulse 40 is supplied to the common X electrode after supplying of the equalizing pulse.

Further, a field bloc having plurality of sub-fields are provided, and a full writing electric discharge and a fine line erasing electric discharge are performed in the first sub-field of each field block for reducing the number of electric discharging.



EP 0 836 171 A2

Description

BACKGROUND OF THE INVENTION

The invention relates to plasma display, driving apparatus of plasma display panel and driving system thereof for, for example, display apparatus of personal computer or workstation, a flat type wall hanging television receiver and a display apparatus for advertising and information. This invention is preferably applicable to AC type plasma display.

In plasma display, one field is divided into several sub-fields, and each pixel (cell) is given forth light by exciting a phosphor by ultraviolet rays that are generated by a electric discharge carried out in the cell. The cell that emits light is practiced by an address electric discharge between two set of electrodes which are provided perpendicular to each other on a front side glass substrate and a back side glass substrate respectively and are capable of driving independently.

A first prior art relating to a plasma display is disclosed, for example, Japanese Patent Application Laid-Open No. 1994/186927. In the prior art, the condition of the electric charge particles in all cells is equalized for surely prohibiting lighting for some cells which are not intended to emit light, and two sets of light emitting discharges, that is, a full writing electric discharge and a full erasing electric discharge in each sub-field are carried out so as to be able to use low voltage for an address electric discharge. Therefore, the contrast is deteriorated because light emitting occurs on full panel when black is displayed.

A second prior art is disclosed, for example, Japanese Patent Application Laid-Open No. 1995/49663. In the prior art, the plurality of the sub-fields having the same brightness gradations are arranged to construct a sub-field block, and several blocks are provided. In sub-field blocks, a preliminary discharge including a full writing electric discharge and a fine line erasing electric discharge is performed in one sub-field, and a writing electric discharge and a erasing electric discharge to a pixel is carried out one time. Therefore, deterioration of a panel is reduced and contrast of a display is improved. The second prior art discloses one of the solution to improve the contrast, but no prior art is shown to improve the contrast in which plural sub-fields having different brightness gradations for constructing one sub-field block.

About 3 μ sec to 4 μ sec is needed to write one line of plasma panel, and an ordinal television display has 480 lines. A writing period of a screen is 1.44 msec if a writing period of one line is 3 μ sec, so that 1.44 msec \times 9=13 msec is needed for one field. The period of one field is 16.7 msec. A sustaining period is 16.7 msec minus a writing period and preliminary discharge period, and this period is not so long enough. Further, if a display has 760 lines per a screen like a high definition display, or if a display has 8 sub-fields for constructing

256 gradations, period for writing is not sufficient.

SUMMARY OF THE INVENTION

It is an object of the present invention to improve contrast.

It is another object of the present invention to improve contrast by reducing a full erasing electric discharge and full writing electric discharge.

It is still another object of the invention to improve contrast by reducing preliminary discharge without changing the number of sub-fields.

According to a feature of the present invention, to achieve the above object, a plasma display and a plasma display driving system include a first electrode group arranged on a permeable substrate and being capable of driving in common, a second electrode group arranged in parallel with the first electrode group on the permeable substrate and being capable of driving independently, a third electrode group arranged perpendicular to the first and second electrode groups on the other substrate and being capable of driving independently, and the plasma panel and the driving system comprising means for performing at least one electric discharge for equalizing electric charge particles in a cell in which another electric charge particle is caused beforehand.

According to another feature of the present invention, to achieve the above object, a plasma display, and a plasma display panel driving system and circuit include a first electrode group driven in common, a second electrode group driven independently, a third electrode group for an address electric discharge, means for erasing and polarizing electric charge particles by a fine line erasing pulse after sustaining period and for supplying an equalizing pulse to one of the electrode group of the first and second electrode group to which the last fine line erasing pulse is supplied and for supplying a regulating pulse to the other electrode of the first and the second electrode groups after supply of the equalizing pulse, thereby controlling the electric charge particles without full erasing electric discharge and full writing electric discharge and improving contrast without light emitting discharge in case of displaying of black color.

According to still another feature of the present invention, to achieve the above object, a plasma display and a plasma display panel driving system and circuit include means for constructing a field block from plurality of sub-fields and for performing a full writing electric discharge and a fine line erasing electric discharge in first sub-field of the field block for decreasing number of electric discharge, means for gathering positive electric charge particles in the vicinity of an address electrode by the full writing electric discharge and fine line erasing electric discharge thereby decreasing a voltage of an address pulse, and means for reproducing the condition of electric charge particles the same as the condition after performing full writing electric discharge and fine

line erasing electric discharge are performed by utilizing a sustaining electric discharge in a cell in which the address electric discharge occurred, thereby reducing the voltage of a address electric discharge in the next field without the full writing electric discharge and the fine line electric discharge. In a cell having no address electric discharge, the condition of electric charge particles after the full writing electric discharge and fine line erasing electric discharge are performed is maintained during one field, so that it is sufficient to perform one time of the full writing electric discharge and fine line electric discharge.

These and other objects, features and advantages of the present invention will become more apparent from the following description when taken in conjunction with the accompanying drawings

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a exploded perspective view illustrating a plasma display panel of the present invention.

Fig. 2 is a sectional view of a plasma display panel which is illustrated from a direction in accordance with an arrow A of Fig. 1.

Fig. 3 is a sectional view of a plasma display panel which is illustrated from a direction in accordance with an arrow of Fig. 1.

Fig. 4 illustrates electrodes and circuits connected to the electrodes of the plasma display panel of Fig. 1.

Fig. 5(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the first embodiment of the present invention.

Fig. 5(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the first embodiment of the present invention.

Fig. 5(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the first embodiment of the present invention.

Fig. 5(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the first embodiment of the present invention.

Fig. 5(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the first embodiment of the present invention.

Fig. 6 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell is illustrated immediately after a power is supplied and then a equalizing pulse and a protecting pulse are supplied.

Fig. 7 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after performing an address electric discharge is illustrated.

Fig. 8 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine line erasing pulse is illustrated.

Fig. 9 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a equalizing pulse in a second field is illustrated.

Fig. 10 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a regulating pulse in a second field is illustrated

Fig. 11(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 11(b) illustrates a driving wave-form supplied to a colon X electrode in accordance with the second embodiment of the present invention.

Fig. 11(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the second embodiment of the present invention.

Fig. 11(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the second embodiment of the present invention.

Fig. 11(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the second embodiment of the present invention.

Fig. 12(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 12(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the third embodiment of the present invention.

Fig. 12(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the third embodiment of the present invention.

Fig. 12(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the third embodiment of the present invention.

Fig. 12(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the third embodiment of the present invention.

Fig. 13(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the invention.

Fig. 13(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the fourth embodiment of the present invention.

Fig. 13(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the fourth embodiment of the present invention.

Fig. 13(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the fourth embodiment of the present invention.

Fig. 13(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the fourth embodiment of the present invention.

Fig. 14(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the invention.

Fig. 14(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the fifth embodiment of the present invention.

Fig. 14(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the fifth

Fig. 14(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the fifth embodiment of the present invention.

Fig. 14(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the fifth embodiment of the present invention.

Fig. 15(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 15(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the sixth embodiment of the present invention.

Fig. 15(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the sixth embodiment of the present invention.

Fig. 15(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the sixth embodiment of the present invention.

Fig. 15(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the sixth embodiment of the present invention.

Fig. 16(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the invention.

Fig. 16(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the seventh embodiment of the present invention.

Fig. 16(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the seventh embodiment of the present invention.

Fig. 16(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the seventh embodiment of the present invention.

Fig. 16(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the seventh embodiment of the present invention.

Fig. 17(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 17(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the eighth embodiment of the present invention.

Fig. 17(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the eighth embodiment of the present invention.

Fig. 17(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the eighth embodiment of the present invention.

Fig. 17(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the eighth embodiment of the present invention.

Fig. 18(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 18(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the ninth embodiment of the present invention.

Fig. 18(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the ninth embodiment of the present invention.

Fig. 18(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the ninth embodiment of the present invention.

Fig. 18(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the ninth embodiment of the present invention.

Fig. 19 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell is illustrated immediately after a power is supplied and then a equalizing pulse and a regulating pulse are supplied in accordance with the ninth embodiment.

Fig. 20 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after performing a address electric discharge is illustrated in accordance with ninth embodiment of the present invention.

Fig. 21 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine line erasing pulse is illustrated in accordance with the ninth embodiment of the present invention.

Fig. 22 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a equalizing pulse in a second field is illustrated in accordance with the ninth embodiment of the present invention.

Fig. 23 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a regulating pulse in a second field is illustrated in accordance with the ninth embodiment of the present invention.

Fig. 24(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 24(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the tenth embodiment of the present invention.

Fig. 24(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the tenth embodiment of the present invention.

Fig. 24(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the tenth embodiment of the present invention.

Fig. 24(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the tenth embodiment of the present invention.

Fig. 25(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the

present invention.

Fig. 25(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the eleventh embodiment of the present invention.

Fig. 25(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the eleventh embodiment of the present invention.

Fig. 25(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the eleventh embodiment of the present invention.

Fig. 25(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the eleventh embodiment of the present invention.

Fig. 26(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 26(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the twelfth embodiment of the present invention.

Fig. 26(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the twelfth embodiment of the present invention.

Fig. 26(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the twelfth embodiment of the present invention.

Fig. 26(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the twelfth embodiment of the present invention.

Fig. 27(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention.

Fig. 27(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the thirteenth embodiment of the present invention.

Fig. 27(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the thirteenth embodiment of the present invention.

Fig. 27(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the thirteenth embodiment of the present invention.

Fig. 27(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the thirteenth embodiment of the present invention.

Fig. 28(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with a second embodiment of the present invention.

Fig. 28(b) illustrates a driving wave-form supplied to a common X electrode in accordance with the fourteenth embodiment of the present invention.

Fig. 28(c) illustrates a driving wave-form supplied to an address A electrode in accordance with the fourteenth embodiment of the present invention.

Fig. 28(d) illustrates a driving wave-form supplied to a first independent Y electrode in accordance with the fourteenth embodiment of the present invention.

tion.

Fig. 28(e) illustrates a driving wave-form supplied to a second independent Y electrode in accordance with the fourteenth embodiment of the present invention.

Fig. 28(f) illustrates a driving wave-form supplied to a third independent Y electrode in accordance with the fourteenth embodiment of the present invention.

Fig. 28(g) illustrates a driving wave-form supplied to a fourth independent Y electrode in accordance with the fourteenth embodiment of the present invention.

Fig. 29 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying sustaining pulses is illustrated in accordance with a embodiment shown in Fig. 28(a)-28(g) of the present invention.

Fig. 30 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell during discharging by a selection electric discharge pulse is illustrated in accordance with the embodiment shown in Fig. 28(a)-28(g) of the present invention.

Fig. 31 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell during supplying electric charge particles control pulse is illustrated in accordance with the embodiment shown in Fig. 28(a)-28(g) of the present invention.

Fig. 32 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine line erasing pulse is illustrated in accordance with the embodiment shown in fig. 28(a)-Fig. 28(g) of the present invention.

Fig. 33 is a time chart of sub-fields illustrating a driving system in accordance with a third embodiment of the present invention.

Fig. 34 is a time chart of sub-fields illustrating a driving system in accordance with a fourth embodiment of the present invention.

Fig. 35 is a time chart of sub-fields illustrating a driving system in accordance with a fifth embodiment of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

The preferred embodiments of the present invention will be described with using drawings hereinafter.

Fig. 1 illustrates a exploded perspective view of a plasma display panel relating to the first embodiment of the present invention.

A transparent common X electrode 22 and a transparent independent Y electrode 23 are provided under a front glass substrate 21, and a X bus electrode 24 and a Y bus electrode 25 are laminated on respective electrode 21 and 22. A dielectric layer 26 and a protecting

layer 27 such as a acid magnesium (MgO) are provided on these electrodes 22, 23, 24 and 25. An address A electrode 29 provided on a back glass substrate 28 is arranged perpendicular to the common X electrode 22 and the independent Y electrode 23 on the front glass substrate 21. The address A electrode 29 is covered by a dielectric layer 30 and a partition wall 31 arranged parallel to the address A electrode 29 is provided on the electrode 29. A phosphor 32 is coated on the partition wall 31 and the address A electrode 29.

Fig. 2 is a sectional view of a plasma display panel which is illustrated from a direction in accordance with an arrow A of Fig. 1. The address A electrode 29 is arranged in the middle of the two partition walls. A discharge gas such as a neon gas or a xenon gas is filled in a space 33 that is provided between the front glass substrate 21 and the back glass substrate 38.

Fig. 3 is a sectional view of a plasma display panel which is illustrated from a direction in accordance with an arrow of Fig. 1. A border of each cell is shown by a dotted line, and the common X electrode 22 and the independent Y electrode 23 are arranged alternatively. In AC type plasma display panel, the electric charge particles on the dielectric layer in the vicinity of the common X electrode 22 and the independent Y electrode 23 are divided into positive electric charge particles and negative electric charge particles for forming an electric field, so that a discharge is performed by using the electric field.

Fig. 4 illustrates electrodes and circuits connected to the electrodes of the plasma display panel of Fig. 1. The common X electrode 22 is connected to an output terminal or several terminals of a X electrode driving circuit 35 that generates a driving pulse for supplying to the common X electrode 22. Each independent Y electrode 23 is connected respective output terminals of a Y electrode driving circuit 36. Each address A electrode 29 is connected respective output terminals of a A electrode driving circuit 37.

Fig. 5 illustrates a first driving system in accordance with a first embodiment of the present invention. Fig. (a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the invention. In the figure, reference numeral 1 denotes one field, a horizontal axis illustrates time and a vertical axis illustrates a line of the cell. The one field is divided into eight sub-fields, that is, a first sub-field 2 to a eighth sub-field 9. A electric charge particle equalizing period 2a-9a, an address period 2b-9b and a sustaining period 2c-9c are arranged in order in each sub-field. Numbers of electric discharges are allotted for each sub-field and display gradations are determined by the total numbers of the discharges. The order for arranging the sub-fields having predetermined numbers of discharges is free, but in the embodiment, the sub-fields are arranged in order from a sub-field having a fewer numbers of electric discharge.

Fig. 5(a)-Fig. 5(e) illustrate wave-forms of pulses

supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively. In the figure, a pulse wave-form 10 illustrates a part of driving wave-form supplied to the common X electrode 22 in one field. A pulse wave-form 11 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 12 and 13 illustrate a parts of driving wave-form supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 10 which is supplied to the common X electrode 22 during first sub-field includes a regulating pulse 40 lasting from the electric charge particle equalizing period 2a to the address period 2b and the sustaining pulses 41 in the sustaining period 2c. In this embodiment, the voltage of the regulating pulse 40 is lower than the voltage of the sustaining pulses 41. The pulse wave-form 11 which is supplied to one of the address A electrodes 29 illustrates the address pulse 42 in the address period 2b and the address pulse 42 corresponds to the cell to be emitted light. The address pulse 42 is not supplied when there is no cell to be emitted. That is, the address pulses 42 are supplied to the cells to be emitted, and the address pulse 42 is not supplied to the other cells not to be emitted. The pulse wave-forms 12 and 13 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulses 43a, 43b, --- in the electric charge particle equalizing period 2a of the first sub-field, scan pulses 44a, 44b, --- in the address period 2b, sustaining pulses 45a, 45b, --- and fine line erasing pulses 46a, 46b, --- in the sustaining period 2c. In the embodiment, the voltage of the scan pulses 44a, 44b, --- is lower than the voltage of the sustaining pulses 45a, 45b, ---. The fine line erasing pulses 46a, 46b, --- and the equalizing pulses 43a, 43b, --- are supplied to the same electrodes. Further, it is preferable to select the pulse width of the fine line erasing pulses 46a, 46b, --- between 0.5 μ sec-2 μ sec.

The movements of the panel is explained hereinafter. In Fig. 5, electric discharges in all cells are performed between the independent Y electrodes 23 and the common X electrodes 22 by supplying the equalizing pulses 43a, 43b, --- to the independent Y electrodes 23, and negative electric charge particles are formed on the dielectric layer 26 in the vicinity of the independent Y electrode 23 during electric charge particle equalizing period 2a occurring immediately after the power is supplied to the display. The electric discharge by the equalizing pulses 43a, 43b, --- occurs only first one time, and the discharge is not occurred after this. That is, the discharge occurs only one time other than the space 33 of the cell becomes abnormal condition. The regulating pulse 40 is supplied to the common X electrode 22 within the time between 0.3 μ sec-2 μ sec from the rising edge of the equalizing pulses 43a, 43b, ---. The negative electric charge particles are formed in the vicinity of the

common X electrode 22, and the positive electric charge particles are formed in the vicinity of the address A electrode 29.

The reason the time between the rising edge of the equalizing pulses 43a, 43b,---and the rising edge of the regulating pulse 40 is determined as described the above is that : too many negative electric charge particles are gathered in the vicinity of the independent Y electrode 23 and the negative electric charge particles gather in the common X electrode 22 when the time between edges of both pulses 43a, 43b,---and 40 is selected too long. When the time is too short, the negative electric charge particles are not gathered on the independent Y electrode 23 and also the positive electric charge particles are not gathered on the address A electrode 22.

The main purpose for supplying the regulating pulse 40 is to attract the negative electric charge particles toward the common X electrode 22 and to form the positive electric charge particles on the address A electrode 29. Another purpose is to assist electric discharges between the common X electrode 22 and the independent Y electrode 23 when the address electric discharge is performed between the address A electrode 29 and the independent Y electrode 23.

The address electric discharges is performed in the cell which is formed at the cross point of the first line of the independent Y electrode 23 and one of the address A electrode 22 when the scan pulses 44a is supplied to the first line of the independent Y electrode 23 and the address pulse 42 is supplied to one of the address A electrode 29 at the same time, as a result, the positive electric charge particles are gathered on the independent Y electrode 23. On the other hand, no discharge is occurred when the address pulse 42 which corresponds to the scan pulses 44b is not supplied to the second line of the independent Y electrode 23, therefore, no electric charge particle is gathered on the independent Y electrode 23. The address pulses 42 are supplied to the address A electrodes 29 which correspond to the cell to be emitted and are selected all the cells at the cross points of all address A electrodes 29 and the scan pulses 44a or 44b is supplied to the independent Y electrodes 23, so that the electric discharges are performed between the address A electrode 29 and the independent Y electrodes 23.

Next, in the sustaining period 2c, the electric discharges for light emitting are performed by the sustaining pulses 41, 45a, 45b,---between the common X electrode 22 and the independent Y electrodes 23 in the cell in which the positive electric charge particles are gathered on the independent Y electrodes 23 side by the electric discharges performed during address period 2b. After that, electric discharges occur between the independent Y electrodes 23 and the common X electrode 22 by supplying the fine line erasing pulses 46a, 46b,--- to the independent Y electrodes 23 and the electric charge particles in the cells are erased, so that

the all electric charge particles generated for light emitting in the cell are erased. The pulse width of the fine line erasing pulses 46a, 46b,---is a little longer than the electric discharge continuation time, therefore, the negative electric charge particles are gathered on the dielectric layer in the vicinity of the independent Y electrodes 23. In the cells in which no electric discharge is occurred, the erasing discharges are not performed because no electric charge particle is in the cell. Therefore, the negative electric charge particles formed in the vicinity of the independent Y electrodes 23 are kept unchanged.

In this situation, no electric discharge occurs by supplying the equalizing pulses 43a, 43b,--- to the independent Y electrodes 23 because the negative electric charge particles in the cell negate the voltage of the equalizing pulses 43a, 43b, ---and sufficient electric fields needed for electric discharge are not formed. After that, no electric discharge is performed through all sub-fields even if the equalizing pulses are supplied. Therefore, the electric discharges are not performed except the first sub-field immediately after the power on, therefore no light emitting occurs in black display.

Further, for the linearity of display gradations determined by the numbers of sustaining pulses, one electric discharge have less influence more than two electric discharges. According to the present invention, the equalization of electric charge particles is effected by one electric discharge in the cell in which the sustaining electric discharge is performed, therefore, the influence to the linearity for display gradations is very small.

The same driving system is performed during the second sub-field 3 to the eighth sub-field 9 and a screen of one field is formed.

Fig. 6-Fig. 10 are sectional views of the plasma display panel in which the condition of electric charge particles in the cell performed sustaining electric discharge are illustrated from the first sub-field after the power is supplied to the second sub-field until the equalizing pulses and the regulating pulse are supplied. In these figure, reference numeral 60 denotes a positive electric charge particles, reference numeral 61 denotes a negative electric charge particles. Further the condition of electric charge particles is illustrated in a cell at center position of Fig. 6-Fig. 7.

Fig. 6 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell is illustrated immediately after a power is supplied and then a equalizing pulse and a regulating pulse are supplied. The figure illustrate the condition of electric charge particles in the first sub-field after power is supplied at first and then the equalizing pulses 43a, 43b are supplied to the independent Y electrodes 23 and finally the regulating pulse 40 is supplied. The electric discharges in all cells occur between the common X electrode 22 and the independent Y electrodes 23 by supplying the equalizing pulses 43a, 43b,---to the independent Y electrodes 23, and the negative electric

charge particles 61 are gathered on the dielectric layer in the vicinity of the independent Y electrodes 23a and the common X electrode 22 and the positive electric charge particles 60 are gathered on the address A electrodes 29 side.

Fig. 7 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after performing an address electric discharge is illustrated. In Fig. 7, the condition of electric discharges is illustrated after the address pulses 42 is supplied to the address A electrodes 29 and address electric discharges occur between the address A electrode 29 and the independent Y electrode 23. The positive electric charge particles 60 are gathered on the dielectric layer in the vicinity of the independent Y electrode 23 because the voltage of the independent Y electrode 23 is lower than the voltages of the address A electrode 29 and the common X electrode 22. The condition of the electric charge particles are shown in Fig. 7. The electric discharge occurs between the independent Y electrode 23 and the common X electrode 22 by the positive electric charge particles 60 and the first pulse of the sustaining pulses 45a, 45b,---supplied to the independent Y electrode 23. This is a sustain discharge. This time, the negative electric charge particles 61 are gathered around the independent Y electrode 23 and the positive charges 62 are gathered around the common X electrode 22 by the electric discharge generated by the sustaining pulses 45a, 45b. As a result, the sustaining electric discharges occur between the independent Y electrode 23 and the common X electrode 22 by the first pulse of sustaining pulses 41. This electric discharges are repeated during sustaining period 2c.

Fig. 8 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine erase pulse is illustrated. In Fig. 8, the condition of electric discharge after the last sustaining pulses 41 is supplied to the common X electrode 22 and then the fine line erasing pulses 46a, 46b,---are supplied is illustrated.

The condition of the electric charge particles after the discharges are occurred by the final sustaining pulses 41 is the same with the condition shown in Fig. 7.

The pulse width of the fine line erasing pulses 46a, 46b,---is longer than the discharge continuation time, so that negative electric charge particles 61 which move so quickly are gathered on the dielectric layer in the vicinity of the independent Y electrode 23. As a result, separation of the electric charge particles is performed. The positive electric charge particles that move slowly in space float in the cell. The negative charges float discharge space for a while.

Fig. 9 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a equalizing pulse in a second field is illustrated. In Fig. 9, a condition of electric discharge after the equalizing pulses 43a, 43b,---in the second sub-field are supplied is illustrated. The voltage of the

equalizing pulses 43a, 43b, ---are canceled by the negative charges and is not reached to the discharge voltage, so that any discharges is not performed. The voltage of the independent Y electrode 23 is higher than the voltage of the other electrodes, so the negative charges are attracted toward the independent Y electrode 23.

Fig. 10 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a regulating pulse in a second field is illustrated.

Referring to the drawing, the condition of electric charge particles after the regulating pulse 40 is supplied to the common X electrode 22 is illustrated. The negative electric charge particles are gathered on the dielectric layer in the vicinity of the common X electrode 22 and the positive charges are gathered at the address A electrode 29. By this, the same driving as the first sub-field is performed without electric discharge by the equalizing pulses 43a, 43b,---. In this case, the voltage of the equalizing pulses 43a, 43b,--- is reduced by the negative electric charge particles at the independent Y electrode 23, so that the electric discharge between the independent Y electrode 23 and the common X electrode 22 is not performed.

The driving of the panel is capable of performing without using the full writing electric discharge and fine line erasing electric discharge for each sub-field. As a result, unnecessary light emitting is erased for displaying black brightness so that the contrast is improved.

The second embodiment of the present invention is described hereinafter. Fig. 11 illustrates a second driving system in accordance with a second embodiment of the present invention. Fig. (a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis shows time and a vertical axis shows lines of cells.

Fig. 11(b)-Fig. 11(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 70 illustrates a part of driving wave-form supplied to the common X electrode 22 in one field. A pulse wave-form 71 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 72 and 73 illustrate parts of driving wave-form supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 70 which is supplied to the common X electrode 22 during first sub-field includes a regulating pulse 40 lasting from the electric charge particle equalizing period 2a to the address period 2b and the sustaining pulses 41 and a fine line erasing pulse 74 in the sustaining period 2c. The pulse wave-form 71 which is supplied to one of the address A electrodes 29 illustrates the address pulse 42 in the address period 2b

which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 71 and 72 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulse 43a, 43b,--- in the electric charge particle equalizing period 2a of the first sub-field, scan pulse 44a, 44b,---in the address period 2b, sustaining pulses 45a, 45b,--- and a first fine line erasing pulses 75a, 75b,--- in the sustaining period 2c.

Under these circumstances, the pulse width of the first fine line erasing pulses 75a, 75b,---is the same with or shorter than a pulse width of a second fine line erasing pulse 74. The number of the fine line erasing pulses is an even number as shown in Fig. 11, that is, the first and the second fine line erasing pulses 75a, 75b and 74, the first fine line erasing pulses 75a, 75b,---which are the last erasing pulses and the equalizing pulses 43a, 43b,---are supplied to the same electrodes, that is, the independent Y electrode 23 of Fig. 11. The second fine line erasing pulse 74 is supplied to the other electrode, that is, the common X electrode 22.

In the embodiment, the last sustaining pulse is supplied to the independent Y electrode 23. A condition of electric charge particles after supplying the first fine line erasing pulses 75a, 75b,---is almost same as the condition shown in Fig. 8. in accordance with the first embodiment. The condition of electric discharges in the other sub-fields 3-9 are the same condition. Further, the erasing and polarizing of electric charge particles are performed by these fine line erasing pulses, so these erasing pulses are called polarization pulse group. In this embodiment, by using the first and the second fine line erasing pulses 75a, 75b and 74, the erasing and polarization are effectively performed, and electric discharging time during address discharging time is maintained constant.

The third embodiment of the present invention is described hereinafter. Fig. 12 illustrates a third driving system in accordance with a third embodiment of the present invention. Fig. 12(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells.

Fig. 12(b)-Fig. 12(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 80 illustrates a part of driving wave-form supplied to the common X electrode 22 in first sub-field. A pulse wave-form 81 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 82 and 83 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 80 which is supplied to the common X electrode 22 during the first sub-field includes a regulating pulse 40 lasting from the electric charge particle equalizing period 2a to the address period 2b and the sustaining pulses 41 in the sustaining period 2c and second fine line erasing pulse 84. The pulse wave-form 81 which is supplied to one of the address A electrodes 29 illustrates the address pulse 42 in the address period 2b which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 82 and 83 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes electric charge particles equalizing pulse 43a, 43b,--- in the electric charge particles equalizing period 2a of the first sub-field, scan pulses 44a, 44b,---in the address period 2b, sustaining pulses 45a, 45b,--- and a third fine line erasing pulses 85a, 85b,--- and a first fine line erasing pulses 86a, 86b,--- in the sustaining period 2c.

Under these circumstances, the pulse width of the second fine line erasing pulses 84 is the same as or shorter than a pulse width of a third fine line erasing pulses 85a, 85b,---.

And a pulse width of the first fine line erasing pulses 86a, 86b,---is the same as or shorter than a pulse width of the second fine line erasing pulse 84

If the numbers of the fine line erasing pulses are an odd number as shown in Fig. 12, that is, the first to the third fine line erasing pulses, the first fine line erasing pulses 86a, 86b,---which are the last supplied erasing pulses and the equalizing pulses 43a, 43b,---are supplied to the same electrodes, that is, the independent Y electrode 23 of Fig. 12. The third fine line erasing pulse 85 which is first supplied fine line erasing pulse is supplied to the same electrode to which first supplied fine line erasing pulses 86a and 86b are supplied, that is, the independent Y electrode 23. Therefore, the last sustaining pulse is supplied to the common X electrode 22. A condition of electric charge particles after supplying the first fine line erasing pulses 86a, 86b,---is almost same condition as shown in Fig. 8 in accordance with the first embodiment. The condition of electric discharges in the other sub-fields 3-9 is the same condition. In the embodiment, by using the first, the second and the third fine line erasing pulses 86a, 86b, 84, 85a and 85b, the erasing and polarization are more effectively performed, and electric discharging time during address discharging time is maintained constant. According to an experiment by present inventors, it is effective for erasing up to three fine line erasing pulses and using more than four fine line erasing pulses is not so effective.

The fourth embodiment of the present invention is described hereinafter. Fig. 13 illustrates a fourth driving system in accordance with a fourth embodiment of the present invention. Fig. 13(a) is a time chart illustrating

an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells.

Fig. 13(b)-Fig. 13(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 90 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 91 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 92 and 93 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 90 which is supplied to the common X electrode 22 during the first sub-field includes a regulating pulse 94 lasting from the electric charge particles equalizing period 2a to the address period 2b and the sustaining pulses 41 in the sustaining period 2c.

The voltage of the regulating pulse 94 and the voltage of the sustaining pulses 41 are the same, thereby a driving circuit is simplified because the same power is used.

The pulse wave-form 91 which is supplied to one of the address A electrodes 29 illustrates the address pulses 42 in the address period 2b which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 92 and 93 which are supplied to the first wave-forms 92 and 93 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulse 43a 43b,--- in the electric charge particle equalizing period 2a of the first sub-field, scan pulse 44a, 44b,---in the address period 2b, sustaining pulses 45a, 45b,--- and fine line erasing pulses 46a, 46b,--- in the sustaining period 2c. A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition. In the embodiment, the voltage of the regulating pulse 94 supplied to the common X electrode 22 and the voltage of the sustaining pulses are the same, therefore simplifying the driving circuit construction.

The fifth embodiment of the present invention is described hereinafter. Fig. 14 illustrates a fifth driving system in accordance with a fifth embodiment of the present invention. Fig. 14(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case

of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 14(b)-Fig. 14(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 100 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 101 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 102 and 103 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 100 which is supplied to the common X electrode 22 during the first sub-field includes a regulating pulse 94 lasting from the electric charge particle equalizing period 2a to the address period 2b and the sustaining pulses 41 in the sustaining period 2c. The voltage of the regulating pulse 94 and the voltage of the sustaining pulses 41 are the same as the fourth embodiment shown in Fig. 13, thereby a driving circuit is simplified because the same power is used. The pulse wave-form 101 which is supplied to one of the address A electrodes 29 illustrates the address pulses 42 in the address period 2b which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 102 and 103 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes electric charge particles equalizing pulse 43a 43b,--- in the electric charge particle equalizing period 2a of the first sub-field, scan pulses 104a, 104b,---in the address period 2b, sustaining pulses 45a, 45b,--- and fine line erasing pulses 46a, 46b,--- in the sustaining period 2c. The voltage of the independent Y electrode 23 during the address period 2c and the voltage of the sustaining pulses 45a, 45b,--- are the same, thereby a driving circuit is simplified because the same power is used. A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric charge particles in the other sub-fields 3-9 is the same the condition.

The sixth embodiment of the present invention is described hereinafter. Fig. 15 illustrates a sixth driving system in accordance with a fifth embodiment of the present invention. Fig. 15(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 15(b)-Fig. 15(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 110 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 111 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 112 and 113 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 111 which is supplied to the common X electrode 22 during the first sub-field includes a first regulating pulse 114 in the electric charge particle equalizing period 2a, a second regulating pulse 115 in the address period 2b and the sustaining pulses 41 in the sustaining period 2c. In the embodiment, the regulating pulse supplied to the common X electrode 22 is divided into the first regulating pulse 114 in the electric charge particle equalizing period 2a and the second regulation pulse 115 in the address period 2b. The pulse wave-form 111 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 112 and 113 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulses 43a, 43b,--- in the electric charge particle equalizing period 2a of the first sub-field, a scan pulse 114a, 114b,---in the address period 2b, sustaining pulses 45a, 45b,--- and fine line erasing pulses 46a, 46b,--- in the sustaining period 2c. A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition. In Fig. 15, the falling edge of the first regulating pulse is slightly earlier than that of the equalizing pulse 43a, thereby preventing electric discharging by mistake between the common X electrode 22 and the independent Y electrode. Further, a rising edge of the second regulating pulse and a rising edge of the scan pulses 115 is effected at the same time, thereby preventing electric discharge by mistake between the common X electrode 22 and the independent Y electrode 23.

The first regulating pulse 114 supplied to the common X electrode 22 and the sustaining pulses 41 may use the same voltage.

The seventh embodiment of the present invention is described hereinafter. Fig. 16 illustrates a seventh driving system in accordance with a seventh embodiment of the present invention. Fig. 16(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a

vertical axis illustrates line of cells. Fig. 16(b)-Fig. 16(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 130 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 131 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 132 and 133 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 131 which is supplied to the common X electrode 22 during the first sub-field includes a first regulating pulse 134 in the electric charge particle equalizing period 2a, a second regulating pulse 135 in the address period 2b and the sustaining pulses 41 in the sustaining period 2c. The pulse wave-form 131 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b of the first sub-field which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 132 and 133 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulse 136a, 136b,--- in the electric charge particle equalizing period 2a of the first sub-field, a scan pulse 137a, 137b,---in the address period 2b, sustaining pulses 45a, 45b,--- and fine line erasing pulses 46a, 46b,--- in the sustaining period 2c.

A falling edge of the equalizing pulse 136a, 136b,--- becomes zero voltage within the time less than 1 μ s in accordance with the present embodiment which is different from above mentioned embodiment. As the voltage of the second regulating pulse 135 and the scan pulses 137a, 137b, the same voltage of the sustaining pulses 41, 45a, 45b,---is able to apply. A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition.

In the embodiment, the edge of the first regulating pulse 139 falls before the edge of the equalizing pulse falls as shown in Fig. 16, thereby preventing electric discharge by mistake between the common X electrode 22 and the independent Y electrode.

The eighth embodiment of the present invention is described hereinafter. Fig. 17 illustrates a eighth driving system in accordance with a eighth embodiment of the present invention. Fig. 17(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical

axis illustrates line of cells. Fig. 17(b)-Fig. 17(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 140 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 141 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 142 and 143 illustrate part of driving wave-form supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 141 which is supplied to the common X electrode 22 during the first sub-field includes a first regulating pulse 144 in the electric charge particle equalizing period 2a, a second regulating pulse 145 in the address period 2b and the sustaining pulses 41 in the sustaining period 2c.

According to the present embodiment, the voltage of the first regulating pulse 144 is set higher than the voltage of the sustaining pulses 41. The pulse wave-form 141 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b of the first sub-field which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 142 and 143 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a electric charge particle equalizing pulse 136a, --- in the electric charge particle equalizing period 2a of the first sub-field, a scan pulse 137a, 137b, --- in the address period 2b, a sustaining pulses 45a, 45b, --- and fine line erasing pulses 46a, 46b, --- in the sustaining period 2c.

The voltage of the second regulating pulse 144 and the scan pulses 137a, 137b, --- can be set the same voltage of the sustaining pulses 41, 45a, 45b, ---. A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b, --- is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same with the condition.

According to the present embodiment, the voltage of the first regulating pulse 144 is higher than that of the sustaining pulses 41. By using higher voltage of the first regulating pulse 144, a lot of negative electric charge particles can be collected, and as a result, a lot of positive electric charge particles are collected on the address electrode 29 side, thereby address discharging is performed so easily.

The ninth embodiment of the present invention is described hereinafter. Fig. 18 illustrates a ninth driving system in accordance with a ninth embodiment of the present invention. Fig. 18(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a divi-

sion of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 18(b)-Fig. 18(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 150 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 151 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 152 and 153 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 151 which is supplied to the common X electrode 22 during the first sub-field includes a equalizing pulse 153 in the electric charge particle equalizing period 2a, a second regulating pulse 154 in the address period 2b and the sustaining pulses 41 and a fine line erasing pulse 155 in the sustaining period 2c.

According to the present embodiment, the only one fine line erasing pulse 155 is provided. The equalizing pulse 153 is supplied to the electrode to which the fine line erasing pulse 155 is supplied such as the case of the fifth embodiment. In case the numbers of the fine line erasing pulses are two or three as shown in the second and the third embodiment (see Fig. 11 and Fig. 12), the equalizing pulses are supplied to the electrode to which the last fine line erasing pulse is supplied.

The pulse wave-form 151 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b of the first sub-field which corresponds to the light emitting cell. The address pulses 42 are not supplied when there is no cell to be emitted. The pulse wave-forms 152 and 153 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 include a first regulating pulse 156a, 156b, --- in the electric charge particle equalizing period 2a of the first sub-field, a scan pulse 137a, 137b, --- in the address period 2b, sustaining pulses 45a, 45b, --- in the sustaining period 2c.

In the embodiment, the first regulating pulses 156a, 156b, --- are supplied within the lapse of time 0.3 μ sec to 2 μ sec from the rising edge of the equalizing pulse 153.

The voltage of the second regulating pulse 144 and the scan pulses 137a, 137b, --- can be set the same voltage of the sustaining pulses 41, 45a, 45b, --- as shown in the fifth embodiment (see Fig. 14). A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b, --- is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same with the condition.

According to the present embodiment, the first regulating pulse 43a, 43b, --- are similar to the equalizing

pulse 43a, 43b,---as shown in Fig. 15, but a equalizing pulse according to the present invention is a pulse that rises first during electric charge particle equalizing period. The reason for providing 0.3 μ sec to 2 μ sec period between the rising edge of the equalizing pulse 153 and the rising edge of the first regulating pulse 156a, 156b,---has already explained.

From Fig. 19 to Fig. 23 are sectional views of the plasma display panel in accordance with the ninth embodiment in which the condition of electric charge particles in the cell performed electric discharge for light emitting are illustrated from the first sub-field after the power is supplied to the second sub-field until the equalizing pulses and the regulating pulse are supplied. In these drawings, reference numeral 60 denotes a positive electric charge particle, reference numeral 61 denotes a negative electric charge particle. Further the condition of electric charge particles is illustrated in a cell at center position of Fig. 19 through Fig. 23.

Fig. 19 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell is illustrated immediately after a power is supplied and then a equalizing pulse and a protecting pulse are supplied. The figure illustrate the condition of electric charge particles in first sub-field after power is supplied at first and then the equalizing pulses 153 are supplied to the common X electrode 22 and finally the first regulating pulses 156a, 156b,--- are supplied. The electric discharge occurs between the common X electrode 22 and the independent Y electrodes 23 by supplying the equalizing pulses 153 to the independent Y electrodes 23 in all cells, and the negative electric charge particles 61 are gathered on the dielectric layer in the vicinity of the independent Y electrodes 23a and the common X electrode 22 and the positive electric charge particles 60 are gathered on the address A electrodes 29 side.

Fig. 20 is a sectional view of a plasma display panel in accordance with the ninth embodiment in which a condition of electric charge particles in a cell after performing an address electric discharge is illustrated. In Fig. 20, the condition of electric discharges is illustrated after the address pulses 42 is supplied to the address A electrodes 29 and address electric discharges are performed between the address A electrode 29 and the independent Y electrode 23. The positive electric charge particles 60 are gathered on the dielectric layer in the vicinity of the independent Y electrode 23 because the voltage of the independent Y electrode 23 is lower than the voltages of the address A electrode 29 and the common X electrode 22. The negative electric charge particles 61 are gathered on the other electrode side.

The condition of the electric charge particles is shown in Fig. 20. The sustaining electric discharge occurs between the independent Y electrode 23 and the common X electrode 22 by the positive electric charges particles 60 and the first pulse of the sustaining pulses 45a, 45b,---supplied to the independent Y electrode 23.

This is a sustain discharge.

Fig. 21 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine line erasing pulse is illustrated. In Fig. 21, the condition of electric discharge after the last sustaining pulses 45a, 45b,---are supplied to the independent Y electrode and then the fine line erasing pulses 155 are supplied to the common X electrode 22 is illustrated.

The pulse width of the fine line erasing pulse 155 is longer than the discharge continuation time, so that negative electric charges particles 61 which move so quickly are gathered on the dielectric layer in the vicinity of the common X electrode 22. The positive electric charge particles that move slowly in space float in the cell. The negative charges float discharge space for a while.

Fig. 22 is a sectional view of a plasma display panel in accordance with the ninth embodiment in which a condition of electric charge particles in a cell after supplying a equalizing pulses in a second field is illustrated.

In Fig. 22, a condition of electric discharge after the equalizing pulse 153 in the second sub-field is supplied is illustrated. The voltage of the equalizing pulse 153 is canceled by the negative charge particles 61 and is not reached to the discharge voltage, so that any electric discharge is not occurred.

Fig. 23 is a sectional view of a plasma display panel in which a condition of electric charges in a cell after supplying a regulating pulse in a second field is illustrated.

In the figure, the condition of electric charge particles after the first regulating pulses 156a, 156b,--- are supplied to the independent Y electrode 23 is illustrated. The negative electric charge particles 61 are gathered on the dielectric layer in the vicinity of the common X electrode 22 and the independent Y electrode 23, and the positive electric charge particles 60 are gathered on the address A electrode 29. By this, the same driving as the first sub-field is performed without electric discharge by the equalizing pulses 153.

The tenth embodiment of the present invention is described hereinafter. Fig. 24 illustrates a tenth driving system in accordance with a tenth embodiment of the present invention. Fig. 24(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 18. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 24(b)-Fig. 24(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 160 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 161 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 162 and 163

illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 160 which is supplied to the common X electrode 22 during the first sub-field includes a equalizing pulse 163 in the electric charge particle equalizing period 2a, a second regulating pulse 165 which continues from equalizing pulse 164 in the electric charge particle equalizing period 2a and the address period 2b and the sustaining pulses 41 and a fine line erasing pulse 155 in the sustaining period 2c.

According to the present embodiment, the only one fine line erasing pulse is provided. The equalizing pulse 164 is supplied to the electrode to which the fine line erasing pulse 155 is supplied such as the case of the first embodiment. In case the numbers of the fine line erasing pulses are two or three as shown in the second and the third embodiment (see Fig. 11 and Fig. 12), the equalizing pulse is supplied to the electrode to which the last fine line erasing pulse is supplied. The pulse wave-form 161 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b of the first sub-field which correspond to the light emitting cell. The address pulses 42 are not supplied when there is no cell to be emitted. The pulse wave-forms 162 and 163 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 includes a first regulating pulse 156a, 156b,--- in the electric charge particle equalizing period 2a of the first sub-field, scan pulses 137a, 137b,---in the address period 2b, sustaining pulses 45a, 45b,--- in the sustaining period 2c. In the embodiment, the first regulating pulses 156a, 156b,---are supplied within the lapse of time 0.3 μ sec to 2 μ sec from the rising edge of the equalizing pulse 153. The voltage of the second regulating pulse 165 and the scan pulses 137a, 137b,---can be set the same voltage of the sustaining pulses 41, 45a, 45b,---as shown in the ninth embodiment (see Fig. 18). The other sub-fields 3-9 are constructed the same as the first sub-field.

A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---are almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition.

According to the present embodiment, the reason for setting the voltage of the equalizing pulse 164 a higher voltage is to collect the negative electric charge particles on the common X electrode side, and to collect lots of positive electric charge particles on the address A electrode 29. Further, the reason for setting the voltage of the second regulating pulse 165 in the address period 2b is to protect electric discharging by mistake between the common X electrode 22 and the independent Y electrode 23.

The eleventh embodiment of the present invention

is described hereinafter. Fig. 25 illustrates a eleventh driving system in accordance with a eleventh embodiment of the present invention. Fig. 25(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 25(b)-Fig. 25(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 170 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 171 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 172 and 173 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 170 which is supplied to the common X electrode 22 during the first sub-field includes a regulating pulse 40 in the period continuing from the electric charge particles equalizing period 2a to the address period 2b and the sustaining pulses 41 in the sustaining period 2c. The pulse wave-form 171 which is supplied to one of the address A electrodes 29 includes a voltage holding pulse 174 whose voltage is determined not to occur a discharge by the scan pulses 44a, 44b,--- and the address pulses 42 in the address period 2b of the first address period 2b which corresponds to the light emitting cell and between the address electrode 29 and the independent Y electrode 23. The address pulses 42 is not supplied when there is no cell to be emitted. According to the present embodiment, the voltage needed for the address electric discharging is the sum of the voltage of the voltage holding pulse 174 and the voltage of address pulses 175, thereby reducing the voltage of the address pulses 175. The address pulses 175 is not supplied when there is no cell to be emitted. The pulse wave-forms 172 and 173 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 include equalizing pulses 43a, 43b,--- in the electric charge particle equalizing period 2a of the first sub-field, a scan pulse 44a, 44b,---in the address period 2b, sustaining pulses 45a, 45b,--- in the sustaining period 2c. The voltage of the regulating pulse 40 and the scan pulses 44a, 44b,---can be determined to the same voltage of sustaining pulses 41, 45a, 45b,---.

A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost the same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition.

The twelfth embodiment of the present invention is

described hereinafter. Fig. 26 illustrates a twelfth driving system in accordance with a twelfth embodiment of the present invention. Fig. 26(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illustrates a division of one field into several sub-fields such as the case of Fig. 5. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 26(b)-Fig. 26(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 180 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 181 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. pulse wave-forms 182 and 183 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The pulse wave-form 180 which is supplied to the common X electrode 22 during the first sub-field includes a regulating pulse 184 in the electric charge particles particle equalizing period 2a, the sustaining pulses 41 in the sustaining period 2c.

The pulse wave-form 181 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b of the first address period which corresponds to the light emitting cell. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 182 and 183 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 include a equalizing pulse 43a, 43b,--- in the electric charging equalizing period 2a of the first sub-field, scan pulses 137a, 137b,---in the address period 2b, sustaining pulses 185a, 185b,---in the address period 2b and sustaining pulses 45a, 45b,--- and fine line erasing pulses 46a, 46b,--- in the sustaining period 2c. The voltage of the regulating pulse 184 can be determined the same the voltage of the sustaining pulses 41.

A condition of electric charge particles after supplying the fine line erasing pulses 46a, 46b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same condition. According to the present embodiment, the voltage of the scan pulses 185a, 185b,---is minus voltage and the voltage of the address pulses 42 is plus voltage, therefore the voltage differences becomes so large that the electric discharge is performed surely.

The thirteenth embodiment of the present invention is described hereinafter. Fig. 27 illustrates a thirteenth driving system in accordance with a thirteenth embodiment of the present invention. Fig. 27(a) is a time chart illustrating an arrangement of sub-fields in one field in accordance with the present invention. The figure illus-

trates a division of one field into several sub-fields such as the case of Fig. 18. A horizontal axis illustrates time and a vertical axis illustrates line of cells. Fig. 27(b)-Fig. 27(e) illustrate wave-forms of pulses supplied to the common X electrode, the address A electrode, and the first and the second independent Y electrode respectively.

A pulse wave-form 190 illustrates a part of driving wave-form supplied to the common X electrode 22 in the first sub-field. A pulse wave-form 191 illustrates a part of driving wave-form supplied to the one of the address A electrode 29. Pulse wave-forms 192 and 193 illustrate parts of driving wave-forms supplied, for example, to a first and a second independent Y electrodes 23.

The wave-form 190 which is supplied to the common X electrode 22 during the eighth sub-field and the field blank period 9d thereafter includes a regulating pulse 40 in the electric charge particle equalizing period 2a and the address period 2b lasting from the equalizing period 2a, sustaining pulses 41 and a full writing pulse 194 in the sustaining period 2c. In the embodiment, the voltage of the full writing pulse 194 is higher enough to be discharged regardless the sustaining discharge is performed or not. As a result, the electric charge particles in all cells are equalized. The field blank period 9d can be provided between the sub-fields. And also, the field blank period 9d is able to arrange several times in one field. The pulse wave-form 191 which is supplied to one of the address A electrodes 29 includes the address pulses 42 in the address period 2b. The address pulses 42 is not supplied when there is no cell to be emitted. The pulse wave-forms 192 and 193 which are supplied to the first electrode of the independent Y electrodes 23 and adjacent second electrode of the independent Y electrodes 23 include the equalizing pulses 43a, 43b,---in the electric charging equalizing period 2a, the scan pulses 44a, 44b,---in the address period 2b, the sustaining pulses 45a, 45b,--- in the sustaining period 2c and fine line erasing pulses 195a, 195b,---in the field blank period 9d. The voltage of the regulating pulse 40 and the scan pulses 44a, 44b, --- can be determined the same as the voltage of the sustaining pulses 41. A condition of electric charge particles after supplying the fine line erasing pulses 195a, 195b,---is almost same condition as shown in Fig. 8 which illustrates the condition of electric charge particles in accordance with the first embodiment. The condition of electric discharge in the other sub-fields 3-9 is the same as the condition.

According to the present embodiment, any electric charge particle in the cells is not remained when black portion continues across several sub-fields, and the address electric discharges are not performed well in the coming address period. To prevent this situation, a discharges are forced by the field full writing pulse 194 between the common X electrode 22 and the independent Y electrode 23.

As explained the above, the plasma display driving system in accordance with the first through the twelfth embodiments can drive the panel without using the full writing electric discharge and erasing discharge for all the cells for equalizing the electric charge particles .

Still another embodiments of the present invention will be explained hereinafter.

Fig. 28 illustrates driving system of a plasma display panel in accordance with a fourteenth embodiment of the present invention. Fig. 28(a) is a time chart illustrating an arrangement of sub-fields in the first sub-field. A horizontal axis illustrates time, and a vertical axis illustrates lines of cells. Fig. 28(b)-Fig. 28(g) are wave-forms illustrating pulse wave-forms supplied to a common X electrode, address A electrode and four independent Y electrodes respectively. In the figure, reference numeral 201 denotes a field, reference numerals 202-203 denote sub-fields, reference numeral 202a-209a denote address periods, reference numeral 202b-209b denote sustaining periods, reference numeral 210-213 denote field blocks, reference numeral 210a-213a denote full writing periods. A wave-form 220 is a driving wave-form supplied to the common X electrode, A wave-form 221 is a driving wave-form supplied to the address A electrode 29, wave-forms 222-225 are driving wave-forms supplied to a first, a second, a third and a fourth electrodes of the independent Y electrodes 23.

In Fig. 28(a), one field period 201 is divided into eight sub-fields 202-209, one field block is arranged by two successive sub-fields, therefore one field period 201 is constructed by four field blocks.

In each field block, the full writing periods 210a, 211a, 212a and 213a which are arranged first period of sub-fields are provided in each first sub-field 202, 204, 206 and 208 of the field blocks 210-213, and following the writing period 210a-213a, the address period 202a, 204a, 206a and 208a and sustaining period 202b, 204b, 206b and 208b are provided. In the second sub-fields 203, 205, 207 and 209 which follow the first sub-fields 202-208, the address period 203a, 205a, 207a and 209a are provided first, and then the sustaining period 203b, 205b, 207b and 209b are provided.

The numbers of light emitting are allotted for each sustaining period 202b-209b, and display graduations are effected by the combinations of the numbers of the light emitting. The numbers of light emitting and the order of the sub-fields are optional. In the embodiment, the numbers of light emitting of the sustaining periods 202b, 204b, 206b, 208b, 203b, 205b, 207b and 209b are arranged in this order from few numbers. The sustaining period 202b, 204b, 206b and 208b just before the sub-fields 203, 205, 207 and 209 in which the full writing erase period are not provided have fewer numbers of light emitting.

Fig. 28(b) illustrates the field block 210, and the other field blocks are constructed similarly. The driving wave-form 220 supplied to the common X electrode 22 includes in the first sub-field 202 a full writing pulse 240

and a polarizing pulse 241 in the first full writing erasing period 210a, a high pulse 242 in the succeeding address period 202a and sustaining pulses 243 and a electric charge particle control pulse 244 and a fine line erasing pulse 245 in the succeeding sustaining period 202b, and further include in the succeeding sub-field a high pulse 246 and sustaining pulses 247.

The voltage of the electric charge particle control pulse 244 and the fine line erasing pulse 245 is the same as or less than the voltage of the sustaining pulses 243. Next to the sustaining pulses 247 is the field block 211. The voltage of the full writing pulse 240 is stepped up a level. The voltage is usually determined about 300 volts, and the reason for stepping up a level is to construct the circuit simply, therefore the stepping up the voltage of full writing pulse 240 is not always necessarily.

The driving wave-form 221 supplied to the address A electrode 29 shown in Fig. 28(a) includes, in the first sub-field, plurality of the address pulses 248a, 248b,--- which relates to the cells to be emitted in the address period 202a, and include, in the succeeding sub-field 203, plurality of address pulses 249a, 249b,--- in the address period 203a.

Fig. 28(d)-28(g) are wave-forms 222,223,224 and 225 supplied to four independent Y electrodes 23 whose electrodes 23 are arranged side by side include, in the first sub-field 202, scan pulses 250a, 250b, 250c, 250d,--- in the address period 202a, sustaining pulses 251a, 251b, 251c, 251d,--- selection electric discharge pulses 252a, 252b, 252c, 252d,---, fine line erasing pulses 253a, 253b, 253c, 263d,--- in the sustaining period 222b and include, in the succeeding sub-field 203, scan pulses 254a, 254b, 254c, 254d,---, in the address period 203a, sustaining pulses 255a, 255b, 255c, 255d,--- in the sustaining period 203b.

The voltage of selection electric discharge pulses 252a, 252b, 252c, 252d,--- is almost the same as the voltage of the electric charge particle control pulse 244, and the electric charge particle control pulse 244 rises with time lag from the rising edge of the selection electric discharge pulses 252a, 252b, 252c, 252d,---, and the delay time t1 is 0.1 μ sec-1.5 μ sec. The electric charge particle control pulse 244 falls earlier than the selection electric discharge pulses 252a, 252b, 252c, 252d,---. The time t2 is about 0.1 μ sec-1.0 sec. The reason for setting the time lag from the rising edge of the selection electric discharge pulses 252a-252d to the rising edge of the electric charge particle control pulse 244 as above mentioned is that, if the time sets longer than that, a lot of negative electric charge particles gathers on the independent Y electrode 23, and a few negative electric charge particles gathers on the common X electrode 22. Further, the reason for rising the selection electric discharge pulses 252a-252d a little earlier than the electric charge particles control pulse 244 is to cause the electric discharge by the selection electric discharging between the common X electrode 22 and

independent Y electrode 23. The reason for falling the electric charge particles 244 is to defend the electric charge between the common X electrode 22 and the independent Y electrode 23 when the selection electric discharge pulses 252a-252d falls.

The selection electric discharge pulses 252a, 252b, 252c, 252d,---, the fine line erasing pulses 253a, 253b, 253c, 253d,--- and the electric discharge particle control pulse 244 are not provided in the succeeded sub-field 203 of the field block 210 of Fig. 28(a).

The sustaining pulses of the sub-field 203 terminate with the sustaining pulses 255a, 255b, 255c, 255d,--- supplied to the independent Y electrodes 23.

The same driving wave-forms are used in the other field blocks 211-213, but the numbers of the sustaining pulses are different. The selection electric discharge pulses, the electric charge particle control pulse and the fine line erasing pulses are provided in the first sub-field 204, 206, 208 of the field blocks 210-213.

The function of the embodiment is explained with Fig. 29-Fig. 32.

The electric discharges occur in all the cells by the full writing pulse 240 supplied to the common X electrode 22 in the field block 210 of Fig. 28(a)-Fig. 28(b) and the electric charge particles are formed. Under these circumstances, the negative electric charge particles 61 are gathered on the address A electrode 29 side. The electric discharge for polarization occurs by the polarization pulse 241, and electric discharge particles on the common X electrode 22 side and the independent Y electrode 23 side are polarized.

The scan pulses 250a of the wave-form 222 are supplied to the first line of the independent Y electrode 23 and at the same time, the address pulses 248a are supplied to the predetermined address A electrode 29 in the succeeded address period 202a, thereby generating full writing electric discharge and forming the electric charge particles in the cell positioned at the cross point of first line of the independent Y electrode 23 and the address A electrode 29, and the positive electric charge particles are gathered on the independent Y electrode 23 side in the cell.

In a similar way, when the scan pulses 250c of the driving wave-form 224 are supplied to the third independent Y electrode 23 and the address pulse 248b is supplied to predetermine address A electrode 29, thereby generating full writing electric discharge and forming the electric charge particles in the cell positioned at the cross point of the third line of the independent Y electrode 23 and the address A electrode 29, and the positive electric charge particles 60 are gathered on the independent Y electrode 23 side in the cell.

The address pulses which correspond to the scan pulses 250b, 250d of the driving wave-form 223, 225 supplied to the second and the fourth independent Y electrode 23 are not supplied when the predetermined cells are not emitted, therefore, writing electric discharges do not occur and electric charge particle is not

formed on the independent Y electrode 23 side.

The sustaining discharge or light emitting discharge in the sustaining period 202b occurs by the sustaining pulses 234 of the driving wave-form 220 and the sustaining pulses 251a, 251b, 251c, 251d,---, of the driving wave-forms 222, 223, 224, 225 in the cell in which the positive electric charge particles are gathered on the independent Y electrode 23 side.

The optional or selecting electric discharges occur by the selection electric discharge pulses 252a, 252b, 252c and 252d in the cell in which the sufficient electric charge particles are formed by the electric discharge for light emitting. The positive electric discharge particles 60 are gathered on the address A electrode 29 side by supplying electric charge particle control pulse 244 to the common X electrode 22 before the electric discharges by the selection electric discharge pulses 252a, 252b, 252c, 252d,---, cease.

After that, the erasing electric discharge is caused by the fine line erasing pulse 245 of the wave-form 220 supplied to the common X electrode 22 and the fine line erasing pulses 253a, 253b, 253c, 253d,---, of the wave-form 222 supplied to the independent Y electrode 23, and the electric charge particles on the common X electrode 22 side and on the independent Y electrode 23 side are mainly erased. Thereby, the condition of the electric charge particles in all cells in which the electric discharges are caused is almost same as the condition of the electric charge particles after the full writing erasing period 210a is finished.

On the other hand, the writing electric discharges or the address electric discharges are not caused in the cells in which the electric discharges for light emitting are not caused and the condition of electric charge particles are the same condition after the full writing erasing period 210a is finished.

As explained above, the electric charge particles in all cells at a point of time after the final erasing pulses 245, 253a, 253b, 253c, 253d,---, are supplied in the first sub-field 202 can be made the same condition after the full writing erasing period 210a is finished. By this, in the succeeding sub-field, address electric discharges in all the cell can be caused without providing a full writing erasing period.

The same functions are repeated in the field blocks 211-213, and a screen of one field is constructed.

Fig. 29-Fig. 32 are sectional views of plasma display panel illustrating a condition of the electric charge particles in a cell in which a sustaining discharge is performed. Referring now to the drawing, wherein like numerals are utilized to designate like parts throughout the several views, reference numeral 60 denotes a positive electric charge particles and reference numeral 61 denotes negative electric charge particles. The condition of the electric charge particles in the drawings is illustrated in a center cell of three cells.

Fig. 29 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell

after supplying sustaining pulses is illustrated in accordance with an embodiment shown in Fig. 28(a)-28(e) of the present invention. The negative electric charge particles 61 are gathered on the dielectric layer 26 of the common X electrode 22 side and the positive electric charge particles 60 are gathered on the dielectric layer 26 of the independent Y electrode 23 side after a final pulse of the sustaining pulses 243 is supplied to the common X electrode 22.

Fig. 30 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell during discharging by a selection electric discharge pulse is illustrated in accordance with the embodiment shown in Fig. 28(a)-28(e) of the present invention. Electric discharges are caused by the voltage of the selection electric discharge pulses 252a, 252b, 252c, 252d, --- and the voltage of positive electric charge particles gathered on the dielectric layer at the independent Y electrode 23 and caused between the independent Y electrode 23 and the common X electrode 22 when selection electric discharge pulses 252a, 252b, 252c, 252d, ---, are supplied to the independent Y electrode. Thereby many positive and negative electric charge particles are generated in the discharging space.

Fig. 31 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell when electric charge particle control pulse is supplied is illustrated in accordance with the embodiment shown in Fig. 28(a)-28(e) of the present invention. The positive electric charge particles are gathered on the address A electrode 29 when the electric charge particle control pulse 244 is supplied to the common X electrode 22 because the voltages of the common X electrode 22 and the independent Y electrode 23 are almost same and the voltages are higher than the voltage of address A electrode 29. Erasing of electric charge particles by erase pulse are still needed because there still remains electric charge particles on the common X electrode 22 side, on the independent Y electrode 23 side and in the discharging space which are not neutralized and not erased.

Fig. 32 is a sectional view of a plasma display panel in which a condition of electric charge particles in a cell after supplying a fine line erasing pulse is illustrated in accordance with the embodiment shown in Fig. 28(a)-28(e) of the present invention. The positive electric charge particles 60 are gathered on the address A electrode 29 side and the negative electric charge particles 61 are gathered on the common X electrode 22 side and the independent Y electrode 23 side. The condition of electric charge particles is the same after the full writing erase period 210a is finished.

A condition of electric charge particles after the full writing erasing period 210a is finished is maintained in the sustaining period 202b because the address electric discharges are not performed in the cells in which no sustaining electric discharge is performed. Also, any electric discharge is not performed by the selection

electric discharge pulses 252a, 252b, 252c, 252d, ---, and there is no change in the condition of electric charge particles even if electric charge particle control pulse 244 and erasing pulse are supplied. Therefore, the condition of electric charge particles in all cells is almost same as the condition after the full writing erasing period 210a and the address electric discharge or a writing electric discharge is caused in next sub-field 203, thereby increasing the contrast by double.

By increasing the voltage of the address pulses 249a and 249b in the sub-field 203, 205, 207, 209 in which the full writing erasing period is not arranged, comparing with the voltage of the address pulses 249a and 249b in the other sub-field 202, 204, 206 and 208, the address electric discharges are caused surely even in the cell in which sustaining electric discharges are not performed, because the positive electric charge particles 60 on the address A electrode 29 side are reduced gradually by neutralization.

Fig. 33 is a time chart of sub-fields illustrating a driving system in accordance with a third embodiment of the present invention. Referring to the drawing, a horizontal axis illustrates time, and a vertical axis illustrates lines of cells. Reference numeral 270 denotes one field period, 271-276 denote sub-fields, 271a-276a denote address periods, 271b-276b denote sustaining periods, 277 and 278 denote field blocks, 277a and 278a denote full writing erasing periods.

One field period 270 is divided into six sub-fields 271-276, and consecutive first three sub-fields 271-273 construct the field block 277 and succeeding consecutive three sub-fields 274-276 construct the other field block 278.

In the first period of these field blocks 277 and 278, the full writing erasing period 277a and 278a are arranged. In each sub-field 271-276, the address period 271a-276a and the sustaining period 271b-276b are arranged but the full writing erasing period 277a and 278a are not provided. That is, the full writing erasing period 277a and 278a are arranged at the first part of the first sub-fields 271 and 274 of the field blocks 277 and 278. The numbers of the light emitting are allotted for the sustaining period 271b-276b, and gradations of display are performed by the combining of the numbers of the light emitting. According to the fifteenth embodiment, the numbers of the light emitting are increased in order of the sub-fields 271, 272, 273.

The selection electric discharge pulse 252a-252d, the electric charge particle control pulse 244 and fine line erasing pulse 245, 253a-253b which are used in the fourteenth embodiment are provided in the first two sub-fields 271, 272, 274, 275 of each field blocks 277 and 278, and these pulses are not provided in the other (last) sub-field. Further, these selection electric discharge pulses, electric charge particle control pulse and fine line erasing pulses are arranged in the last part of the sustaining period 271b, 272b, 274b and 275b, thereby the condition of electric charge particles in all cells after

sustaining period 271b, 272b, 274b and 275b are finished is maintained the same condition after the full writing erasing period 277a is finished, so the full writing erasing period 210a is deleted in the sub-fields 272, 273, 275 and 276 other than the first sub-fields 271 and 274, and the address electric discharges are performed in the address period 272a, 273a, 275a and 276a without supplying the selection electric discharge pulses 252a-252d and electric charge particle control pulse 244 in the last sub-field. Therefore, the contrast is multiplied by three.

Fig. 34 is a time chart of sub-fields illustrating a driving system in accordance with a fifteenth embodiment of the present invention. Referring to the drawing, a horizontal axis illustrates time, and a vertical axis illustrates lines of cells. Reference numeral 280 denotes one field period, 281-286 denote sub-fields, 281a-288a denote address periods, 281b-288b denote sustaining periods, 289 and 290 denote field blocks, 289a and 290a denote full writing erasing periods.

One field period 280 is divided into eight sub-fields 281-288, and consecutive first four sub-fields 281-284 construct the field block 289 and succeeding consecutive four sub-fields 285-288 construct the other field block 290.

The first period of these field blocks 289 and 290 include the full writing erasing period 289a and 290a, and the address period 281a-288a and the sustaining period 281b-288b are arranged in each sub-field 281-288. That is, the full writing erasing period 289a and 290a are arranged at the first part of the first sub-fields 281 and 285 of the field blocks 289 and 290. The numbers of the light emitting are allotted for the sustaining periods 281b-288b, and gradations of display are performed by the combining the numbers of the light emitting. According to the sixteenth embodiment, the numbers of the light emitting are increased in order of the sub-fields 281, 282, 283,---

The selection electric discharge pulse 252a-252d, the electric charge particle control pulse 244 and fin line erasing pulse 245, 253a-253b which are used in the fourteenth embodiment are provided in the sustaining period 281b, 282b, 283b, 285b, 286b and 287b of the first three sub-fields 281, 282, 284, 285, 286 and 287 in each field blocks 287 and 290, and these pulses are not provided in the other(last) sub-field.

The condition of electric charge particles in all cells after sustaining period 281b, 282b and 283b of the sub-fields 281, 282 and 283 in the field block 289 and sustaining periods 285b, 286b and 287b of the sub-fields 285, 286 and 287 in the field block 290 are finished is maintained the same condition after the full writing erasing period 289a is finished, so the full writing erasing periods 210a are deleted in the three sub-fields 282, 283, 284, 286, 287 and 288 other than the first sub-fields 281 and 285, and the address electric discharges are performed in the address period 282a, 273a, 284a, 286a, 287a and 288a without supplying the selection

electric discharge pulses 252a-252d and electric charge particle control pulse 244 in the last sub-field. Therefore, the contrast is multiplied by four.

Fig. 35 is a time chart of sub-fields illustrating a driving system in accordance with a seventeenth embodiment of the present invention. Referring to the drawing, a horizontal axis illustrates time, and a vertical axis illustrates lines of cells. Reference numeral 300 denotes one field period, 301-308 denote sub-fields, 301a-308a denote address periods, 301b-308b denote sustaining periods, 309 denote field blocks, 309 denote a full writing erasing period.

One field period 300 is divided into eight sub-fields 301-308, and the field block 309 is constructed by all sub-fields 301-308 in the one field. The first period of these field blocks 309 includes the full writing erasing period 309a. The address period 301a-308a and the following sustaining period 301b-308b are arranged in each sub-field 301-308. That is, the full writing erasing period 309a are arranged at the first part of the first sub-fields 302. The numbers of the light emitting are allotted for the sustaining period 301b-308b and gradations of display are performed by the combining the numbers of the light emitting.

The selection electric discharge pulses 252a-252d, the electric charge particle control pulses 244 and fin line erasing pulses 245, 253a-253b which are used in the fourteenth embodiment are provided in the sustaining periods 301b-307b of the first seven sub-fields 301-307, and the full writing erasing period 309a is arranged only in the first sub-field 301. The address electric discharges in these address periods 302a-308a are possible, even if the full writing erasing periods 309a in the sub-fields 302-308 which follow the first sub-field 301 is deleted. Thereby the contrast is multiplied by eight.

According to these embodiments, by deleting full writing erasing period in some sub-field, the contrast is improved. The practical contrast of cathode ray tube display is, for example, 150:1, and in the plasma display according to the embodiments shown in Fig. 33 or Fig. 34, corresponding contrast is accomplished.

The numbers of sub-fields in one field and the numbers of the sub-fields in one field block are optional and not limited above mentioned embodiments, and any combination will be applicable.

According to the present invention, full writing electric discharge and erasing electric discharge are deleted or reduced, thereby improving the contrast of the display.

According to the present invention, full writing erasing period can be arranged one time per several sub-fields, thereby improving the contrast.

While we have shown and described several embodiments in accordance with the present invention, it is understood that the same is not limited thereto but is susceptible of numerous changes and modifications as known to those skilled in the art, and we therefore do not wish to be limited to the details shown and

described herein but intend to cover all such change and modifications as are encompassed by the scope of the appended claims.

Claims

1. A plasma display panel driving system for a display panel having a first electrode group arranged on a permeable substrate and being capable of driving in common, a second electrode group arranged in parallel with said first electrode group on said permeable substrate and being capable of driving independently, a third electrode group arranged perpendicular to said first and second electrode groups on another substrate and being capable of driving independently, said driving system comprising the step of performing at least one electric discharge for equalizing of electric charge particle in a cell in which another electric charge particle is caused beforehand.
2. A plasma display panel driving apparatus comprising:
 - a first electrode group arranged on a permeable substrate and being capable of driving in common;
 - a second electrode group arranged in parallel with said first electrode group on said permeable substrate and being capable of driving independently;
 - a third electrode group arranged perpendicular to said first and second electrode groups on another substrate and being capable of driving independently;
 - and means for performing at least one electric discharge for equalizing of electric charge particle in a cell in which another electric charge particles is caused beforehand.
3. A plasma display comprising:
 - a first electrode group arranged on a permeable substrate and being capable of driving in common; a second electrode group arranged in parallel with said first electrode group on said permeable substrate and being capable of driving independently;
 - a third electrode group arranged perpendicular to said first and second electrode groups on another substrate and being capable of driving independently;
 - and means for performing at least one electric discharge for equalizing of electric charge particles in a cell in which another electric charge particles is caused beforehand, whereby improving linearity of display gradation.
4. A plasma display panel driving system for a display panel having a first electrode group arranged on a permeable substrate and being capable of driving in common, a second electrode group arranged in parallel with said first electrode group on said permeable substrate and being capable of driving independently, a third electrode group arranged perpendicular to said first and second electrode groups on another substrate and being capable of driving independently, a equalizing means for equalizing electric charge particles in all cells by a discharge between said first and said second electrode group, an address electric discharge means for determining a cell to be emitted by a discharge between said second and said third electrode groups, and a sustaining electric discharge means for performing light emitting by a electric discharge between said first and said second electrode groups, said driving system comprising the step of performing discharge by a fine line erasing pulse supplied after a sustaining electric discharge is finished and erasing and polarizing of electric charge particles in a cell in which sustaining electric discharge is performed, the step of gathering said electric charge particles in all cells by way of said, electric discharge caused by the fine line erasing pulse, and canceling the voltage of a equalizing pulse supplied to one of said first and said second electrode groups by said electric charge particles resulting in no discharge, the step of polarizing said electric charge particles into a positive electric charge particles and a negative electric charge particles, the step of supplying a regulating pulse to the other electrode groups of said first and said second electrode groups after supplying said equalizing pulse to one of said first and said second electrode groups for gathering the same electric charge particles gathered on said one of electrode groups supplied with said equalizing pulse, and the step of gathering a electric charge particles having the other polarity on said third electrode group
5. A plasma display panel driving system for a display panel having a first electrode group arranged on a first substrate and being capable of driving in common, a second electrode group arranged in parallel with said first electrode group on said first substrate and being capable of driving independently, a third electrode group arranged perpendicular to said first and second electrode groups on a second substrate and being capable of driving independently, said driving system comprising the step of performing a sustaining electric discharge by supplying a sustaining pulse to said first and said second electrode groups, the step of polarizing electric charge particles in a cell by supplying a fine line erasing pulse to one of said first and said second electrode groups, the step of gathering electric charge particles

cles having one of the polarities in vicinity of said first and said second electrode groups and gathering said electric charge particles having the other polarity in the vicinity of said third electrode group by supplying a equalizing pulse to said one of electrode groups and supplying a regulating pulse rising later than said equalizing pulse to the other electrode group of said first and said second electrode groups without performing electric discharge.

6. A plasma panel display driving system according to the claim 4 or 5, further comprising the step of performing a electric discharge between said first electrode group and said second electrode group by supplying said equalizing pulse to said one of the electrode groups after supplying a power, and gathering electric charge particles having one of the polarities in the vicinity of said first and said second electrode groups and gathering electric charge particles having the other polarity in the vicinity of said third electrode group by supplying said regulating pulse after said equalizing pulse is supplied.

7. A plasma display panel driving system according to claim 4 or 5, further comprising the step of performing a address electric discharge between said second electrode group and said third electric group after gathering electric charge particles having one of the polarities in the vicinity of the said first and said second electrode groups and gathering electric charge particles having the other polarity in the vicinity of said third electrode group, and performing sustaining electric discharge.

8. A plasma display panel driving system according to claim 4 or 5, further comprising the step of supplying said regulating pulse to said other electrode group within 0.3 μsec -2 μsec after supplying said equalizing pulse to said one of said electrode groups.

9. A plasma display panel driving system according to claim 4 or 5, further comprising the step of arranging several fine line erasing pulses having a first fine line erasing pulse whose pulse width is within 0.5 μsec -2 μsec and having at least a second fine line erasing pulse whose pulse width is equal to or less than the pulse width of said first pulse width for performing erasing and polarization of said electric charge particle.

10. A plasma display panel driving system according to claim 4 or 8 further, comprising the step of supplying said equalizing pulse to said second electrode group, and maintaining supply of said regulating pulse to said first electrode group until deciding cells to be emitted for gathering electric charge particles predetermined electrode group.

11. A plasma display panel driving system according to claim 4, 5 of 10, further comprising the step of dividing said regulating pulse into a first pulse for equalizing electric charge particles in all cells and a second regulating pulse supplied during a period for deciding cells to be emitted.

12. A plasma display panel driving system according to claim 4 or 5, further comprising the step of selecting the voltage of said regulating pulse substantially equal to the voltage of said sustaining pulse for performing display electric discharge.

13. A plasma display panel driving system according to claim 13, further comprising the step of falling the edge of said equalizing pulse after falling the edge of said first regulating pulse, and falling said edge of said equalizing pulse within 1 μsec .

14. A plasma display panel driving system according to claim 4 or 5, further comprising the step of performing a discharge in all cells by supplying full electric discharge pulse to one of said first and said second electrode groups one or several times per one field or several fields after performing sustaining discharge, and then supplying said fine line erasing pulse for performing a discharge in said all cells for erasing electric charge particles so as to gather positive electric charge particles in the vicinity of said first and said second electrode groups.

15. A plasma display panel driving system according to claim 4 or 5, further comprising the step of supplying plurality of fine line erasing pulses to said first and second electrode groups alternatively, and shortening the pulse width of said fine line erasing pulses which supplied afterward, and supplying the last fine line erasing pulse to said one of electrode group.

16. A plasma display panel driving system according to claim 4, 5 or 8, further comprising the step of supplying said equalizing pulse to one of electrode group, and falling the edge of said equalizing pulse in a time more than 1 μsec .

17. A plasma display panel driving circuit comprising:

a first electrode group arranged on a first substrate and driven in common;
a second electrode group arranged parallel to said first electrode group on said first substrate and controlled independently;
a third electrode group arranged perpendicular to said first and second electrode groups on a second substrate provided faced on said first substrate and controlled independently;
a first driving circuit connected to said first elec-

- trode group for supplying a first driving pulse;
 a second driving circuit connected to said second electrode group for supplying a second driving pulse;
 a third driving circuit connected to said third electrode group for supplying an address driving pulse;
 means for supplying a fine line erasing pulse to one of the electrode group of said first and second electrode groups from said driving circuit connected to said one of the electrode group after sustaining discharging;
 means for supplying a equalizing pulse to said one of the electrode group from said driving circuit connected to said one of the electrode group;
 means for supplying a regulating pulse delaying a bit from rising edge of said equalizing pulse to the other electrode group of said first and said second electrode groups from said driving circuit connected said other electrode group, and for gathering electric charge particles having one of the polarities in the vicinity of said first and said second electrode groups and for gathering electric charge particles having the other polarity in the vicinity of said third electrode group.
18. A plasma display panel driving circuit according to claim 17, further comprising means for supplying said regulating pulse to said other electrode group within 0.3 μ sec-2 μ sec after supplying said equalizing pulse to said one of said electrode groups.
19. A plasma display panel driving circuit according to claim 17 or 18, further comprising means for generating plurality of fine line erasing pulses from said first and said second driving circuits and selecting pulse width of a first fine line erasing pulse within 0.5 μ sec-2 μ sec and selecting pulse width of a second fine line erasing pulse equal to or shorter than said first fine line erasing pulse.
20. A plasma display panel driving circuit according to claim 17, 18 or 19, further comprising means for supplying from said second driving circuit said equalizing pulse to said second electrode group and for supplying said regulating pulse from said first driving circuit to said first electrode group during addressing cells to be emitted.
21. A plasma display panel driving circuit according to claim 17, 18 or 19, further comprising means for generating a first and a second regulating pulses from said driving circuit connected to said one of electrode group.
22. A plasma display panel driving circuit according to

claim 17, further comprising means for selecting the voltage of said regulating pulse substantially identical with the voltage of said sustaining pulse.

23. A plasma display panel driving circuit according to claim 17, 18 or 20, further comprising means for falling the edge of said equalizing pulse after lapse of time more than 1 μ sec by said driving circuit connected to said one of electrode.
24. A plasma display panel driving circuit according to claim 17, further comprising means for supplying after performing sustaining discharge a full writing electric discharge pulse one time or several times per one field or several fields generated from one of driving circuit of said first and said second driving circuit which is connected to said one of electrode group.
25. A plasma display panel driving circuit according to claim 21, further comprising means for falling the edge of said equalizing pulse after falling the edge of said first regulating pulse, and falling said edge of said equalizing pulse within 1 μ sec.
26. A plasma display comprising :
- a first electrode group arranged on a first substrate and driven in common;
 - a second electrode group arranged parallel to said first electrode group on said first substrate and controlled independently;
 - a third electrode group arranged perpendicular to said first and second electrode groups on a second substrate provided faced on said first substrate and controlled independently;
 - cells constructed at the cross point of said first and said second and said third electrode group;
 - means for performing a electric discharge for erasing and polarizing electric charge particles in cells performed a sustaining discharging during a period after sustaining discharging and just before the beginning of an address period;
 - means for equalizing of electric charge particle in all cells by said discharge for erasing and polarizing.
27. A plasma display comprising:
- a first electrode group arranged on a first substrate and driven in common;
 - a second electrode group arranged parallel to said first electrode group on said first substrate and controlled independently;
 - a third electrode group arranged perpendicular to said first and second electrode groups on a second substrate provided faced on said first

substrate and controlled independently;
cells constructed at the cross point of said first and said second and said third electrode group;

means for performing a discharge by a fine line erasing pulse supplied to one of the electrode of said first and said second electrode group after a sustaining discharge and for erasing and polarizing electric charge particles in cells generated by said sustaining discharge;

means for gathering electric charge particles having one of polarities in the vicinity of said first and said second electrode groups and for gathering electric charge particles having the other one of the polarities in the vicinity of said third electrode group by supplying a equalizing pulse to said one of electrodes and by supplying a regulating pulse to the other electrode of said first and said second electrode groups so as to be able to discharge for addressing which determines light emitting cells by said third electrode group.

28. A plasma display according to claim 27, further comprising means for supplying said regulating pulse to said other electrode within 0.3 μ sec-2 μ sec after supplying said equalizing pulse to said one of said electrode groups for collecting electric charge particles having one of polarities in the vicinity of said first and said second electrode group and electric charge particles having the other one of polarities in the vicinity of said third electrode group.

29. A plasma display according to claim 27, further comprising a first driving circuit connected to said first electrode group, a second driving circuit connected to said second electrode group, means for generating plurality of said fine line erasing pulses including a first fine line erasing pulse having pulse width within 0.5 μ sec-2 μ sec and a second fine line erasing pulse having pulse width equal to or less than said second fine line erasing pulse, and means for erasing and polarizing electric charge particles by supplying last pulse of said fine line erasing pulses to said one of said first and second electrode groups.

30. A plasma display according to claim 27, further comprising means for supplying from said second driving circuit said equalizing pulse to said second electrode group and for maintaining supply of said regulating pulse supplied from said first driving circuit to said first electrode group during addressing cells to be emitted.

31. A plasma display according to claim 27, further comprising means for generating from one of said first driving circuit connected said first electrode

group and said second driving circuit connected to said second electrode group a first and a second regulating pulses.

32. A plasma display according to claim 27, further comprising a first driving circuit connected to said first electrode group, a second driving circuit connected to said second electrode group, and means for selecting the voltage of said regulating pulse substantially identical with the voltage of said sustaining pulse by means of one of driving circuit of said first and said second driving circuits.

33. A plasma display according to claim 27, further comprising a driving circuit connected one of said first and said second electrode groups, and means for falling the edge of said equalizing pulse after lapse of time more than 1 μ sec by said driving circuit connected to said one of electrode group.

34. A plasma display according to claim 27, further comprising a first driving circuit connected to said first electrode group, a second driving circuit connected to said second electrode group, and means for generating a full writing pulse one time or several times per one field or several fields from one of driving circuit of said first and said second driving circuit and for performing a discharge in all cells after sustaining discharge is finished.

35. A plasma display panel driving system comprising the step of dividing one field into several sub-fields and providing an address period for setting cells to be emitted and a sustaining period for light emitting, and the step of constructing field block by plurality of sub-fields and arranging full writing electric discharge and full erasing electric discharge in a first sub-field of said field block.

36. A plasma display panel driving system according to claim 35, further comprising the step of performing discharge in selected cells in which light emitting is performed by said sustaining electric discharge in said sub-fields of said sub-field block except the last sub-field of said sub-field thereby keeping the condition of electric charge particles almost same condition after performing full writing electric discharge and full erasing electric discharge.

37. A plasma display panel driving system according to claim 35 or 36, wherein said plasma display pane having a first electrode group covered with a dielectric layer and arranged on a first glass substrate and driven in common, a second electrode group covered with a dielectric layer arranged parallel to said first electrode group on said first glass substrate and controlled independently, a third electrode group covered with a dielectric layer and arranged

perpendicular to said first and said second electrode groups on a second glass substrate provided faced on said first substrate and controlled independently, and said driving system comprising the step of performing discharge in selected cells in which light emitting is performed by said sustaining electric discharge in said sub-fields of said sub-field block except the last sub-field of said sub-field, thereby gathering positive electric charge particles on said dielectric layer covering said third electrode group and gathering negative electric charge particles on said dielectric layer covering said first and said second electrode group for leaving condition of electric charge particle unchanged without selection electric discharge.

38. A plasma display panel driving system according to claim 37, further comprising the step of performing said address electric discharge for determining cells to be emitted by discharging between said first and said second electrode groups and performing said sustaining discharge between said first and second electrode groups, and the step of supplying after supplying said last sustaining pulse to said first and said second electrode groups in all sub-fields of each sub-field block except each last sub-field a first pulse having almost the same voltage as the voltage of said sustaining pulse to one of electrode group of said first and second electrode groups to which the last sustaining pulse is not supplied and performing electric discharge in said cell in which said sustaining electric discharge has performed, and the step of arranging a second pulse rising after 0.3 μ sec-1.5 μ sec from the rising edge of said first pulse and supplying said second pulse to the other electrode group of said first and said second electrode groups to which said first pulse is not supplied, and providing plurality of fine line erasing pulses after supplying said first and said second pulses for erasing electric charge particles in the vicinity of said first and second electrode groups.

39. A plasma display panel driving system according to claim 38, wherein the voltage of said first and said second pulses and said fine line erasing pulse supplied after the supply of said sustaining pulse having a voltage the same as the sustaining voltage selected so as to cause sustaining electric discharge in cells performed said address electric discharge and to cause no sustaining electric discharge in cells in which no electric discharge is not performed.

40. A plasma display panel driving system according to claim 38 or 39, wherein further comprising a first address pulse supplied to cells in which no full writing electric discharge and no full erasing electric

discharge are not performed, and said first address pulse having a voltage higher than a voltage of a second address voltage supplied to the other cells in which said full writing electric discharge and said full erasing electric discharge are performed.

41. A plasma display comprising:

a first electrode group arranged on a first substrate and driven in common;
a second electrode group arranged parallel to said first electrode group on said first substrate and controlled independently;
a third electrode group arranged perpendicular to said first and second electrode groups on a second substrate provided faced on said first substrate and controlled independently;
a field block constructed from plurality of sub-fields;
means for performing a full writing electric discharge between said first and said second electrode groups in a first field of said field block;
means for performing a electric discharge by supplying a selection electric discharge pulse to one of the electrode group of said first and said second electrode groups in said sub-fields of each field block except each last sub-field in cells in which light emitting is performed by a sustaining discharge;
means for gathering electric charge particles having one of polarities in the vicinity of said first and second electrode groups and gathering electric charge particles having the other polarity in the vicinity of said third electrode groups by supplying said selection electric discharge pulse to said one of electrode group and by supplying a electric charge particle control pulse to the other one of said first and second electrode groups;
means for performing no selection electric discharge in cells in which light emitting is not performed by said sustaining electric discharge.

42. A plasma display according to claim 41, further comprising means for supplying a sustaining pulse to said first and said second electrode groups and means for supplying the last sustaining pulse to said other electrode group.

43. A plasma display according to claim 41, wherein the rising edge of said electric charge particle control pulse having time lag of 0.3 μ sec-1.5 μ sec from rising edge of said selection electric discharge pulse.

44. A plasma display according to claim 41, further comprising means for supplying said fine line erasing

ing pulse to said first and said second electrode group after selection electric discharge pulse is supplied.

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FIG. 1

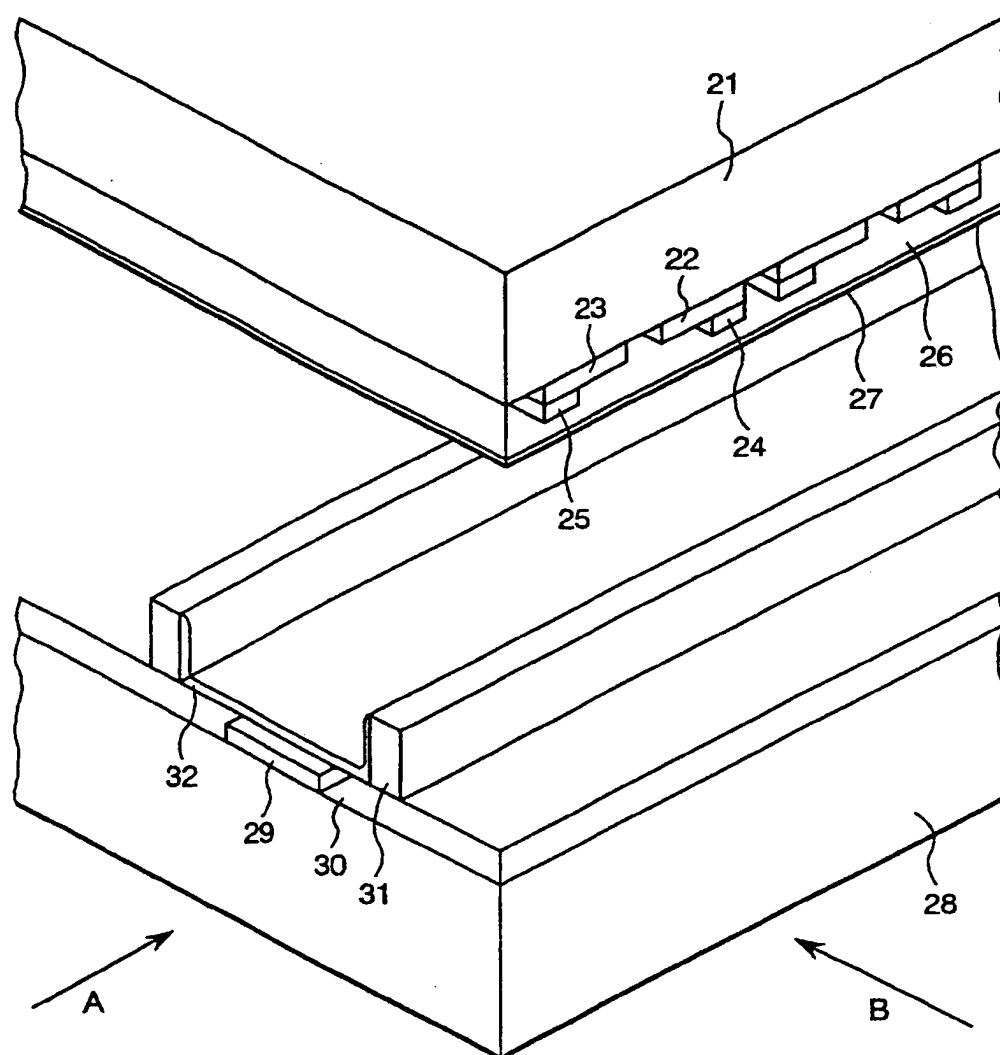


FIG. 2

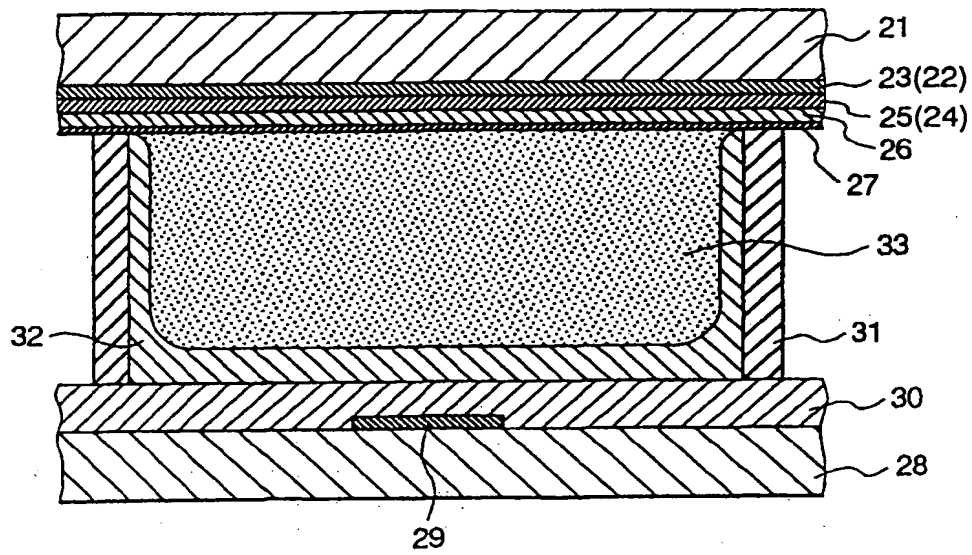


FIG. 3

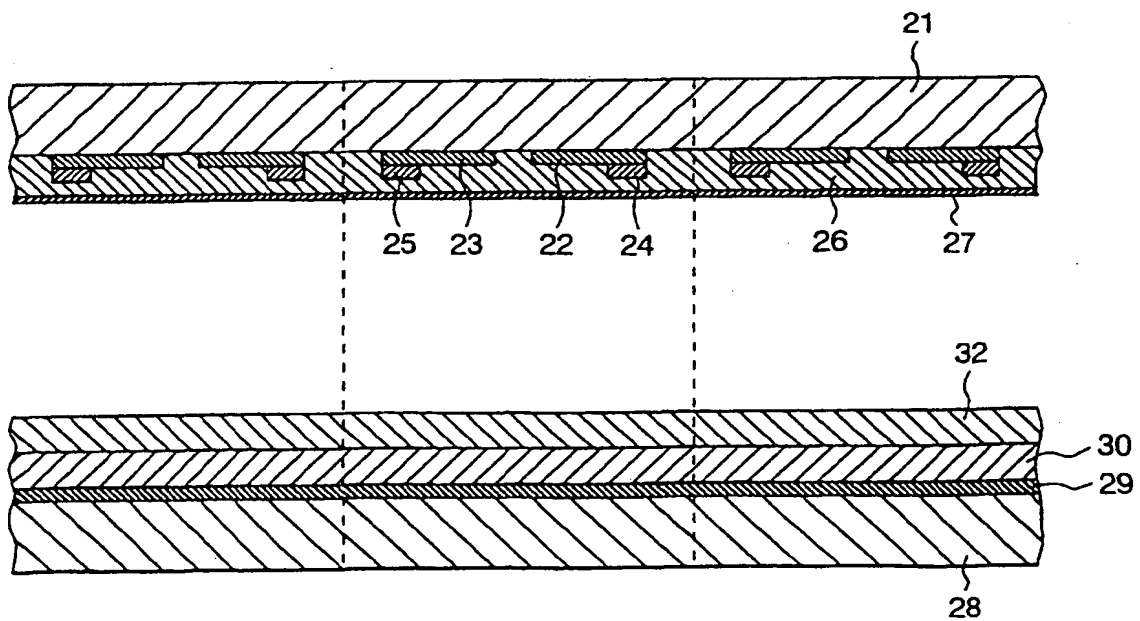


FIG. 4

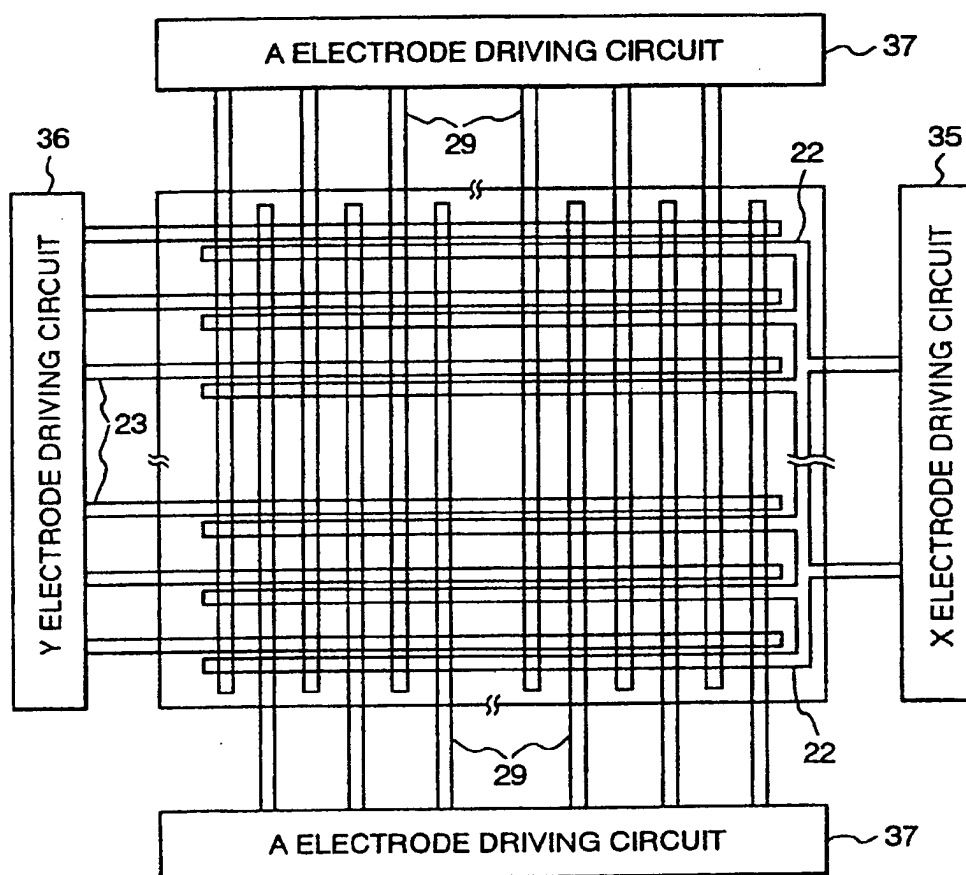


FIG. 5(a)

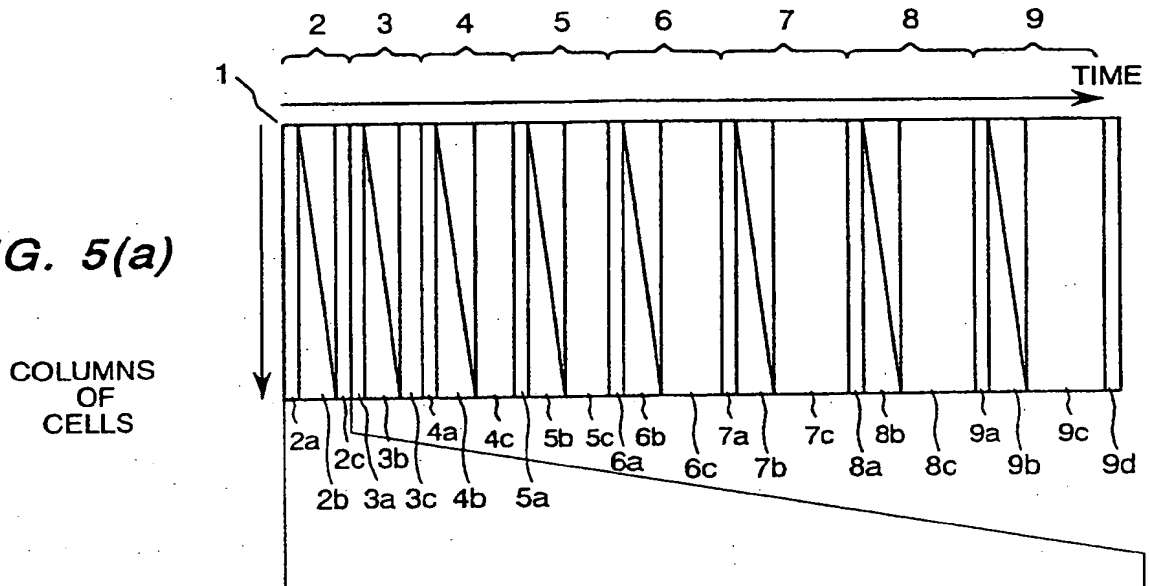


FIG. 5(b)

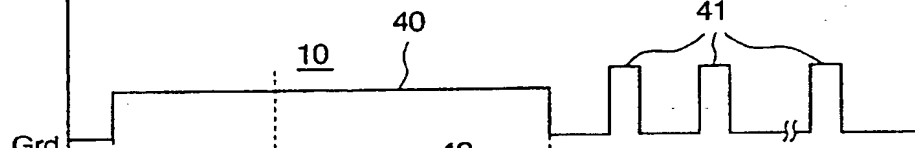


FIG. 5(c)

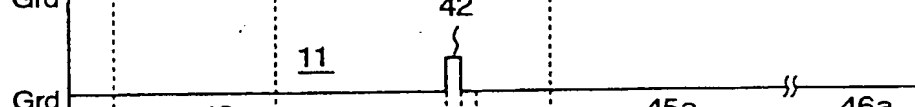
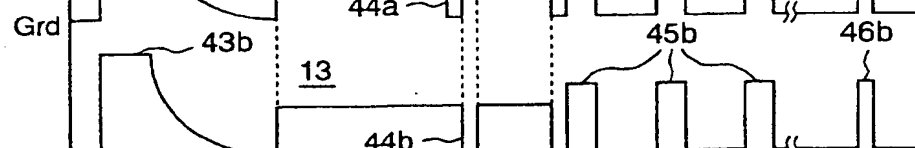


FIG. 5(d)



FIG. 5(e)



EQUALIZING PERIOD

ADDRESS PERIOD

SUSTAIN PERIOD

FIG. 6

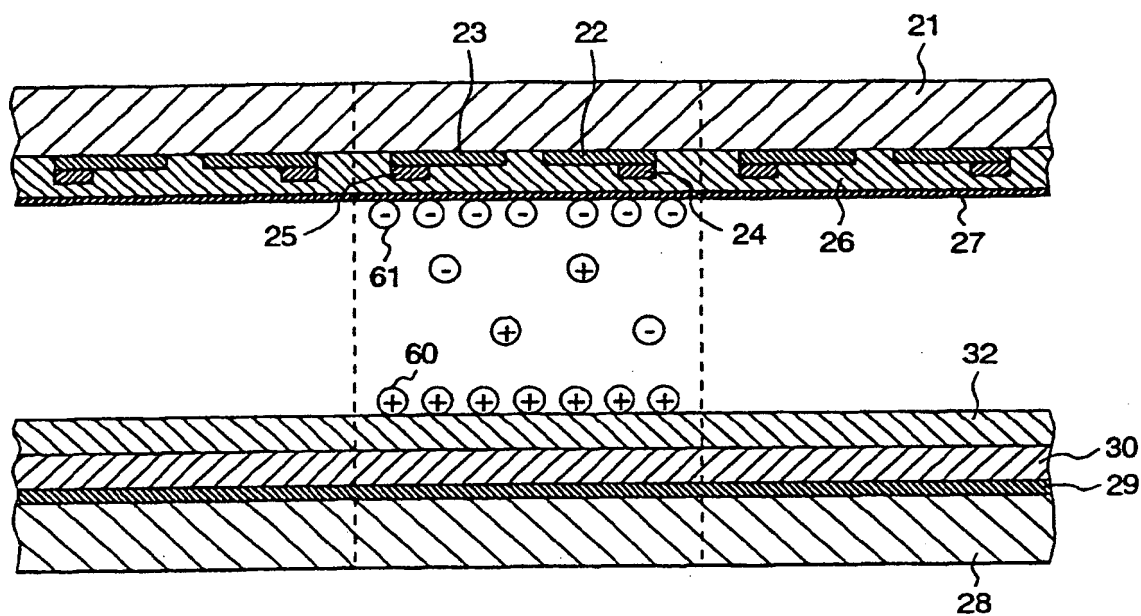


FIG. 7

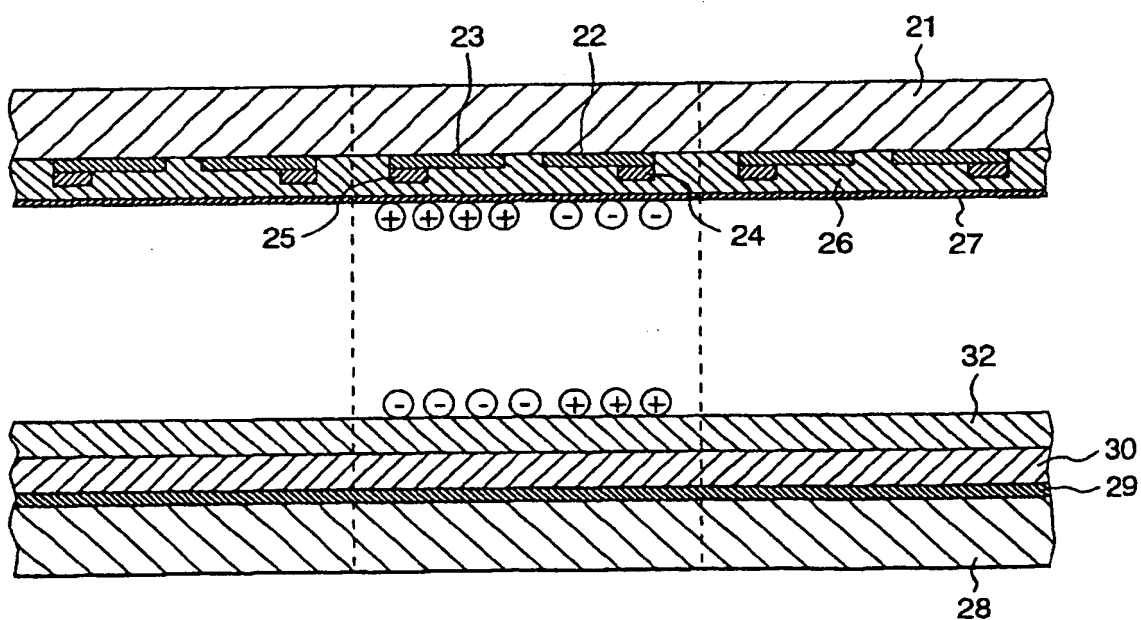


FIG. 8

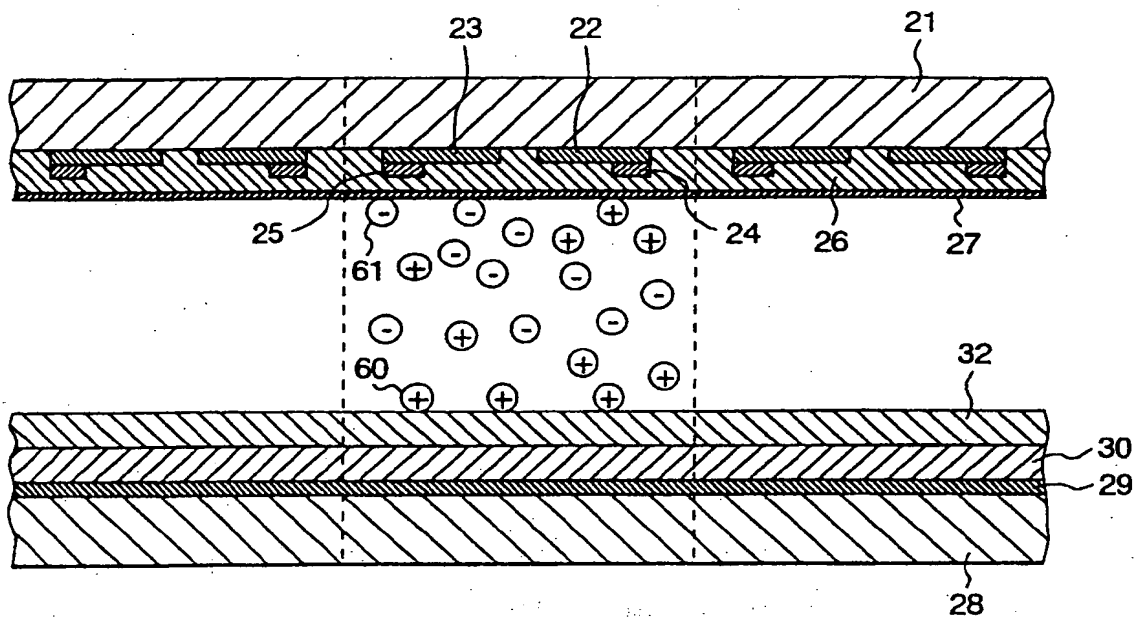


FIG. 9

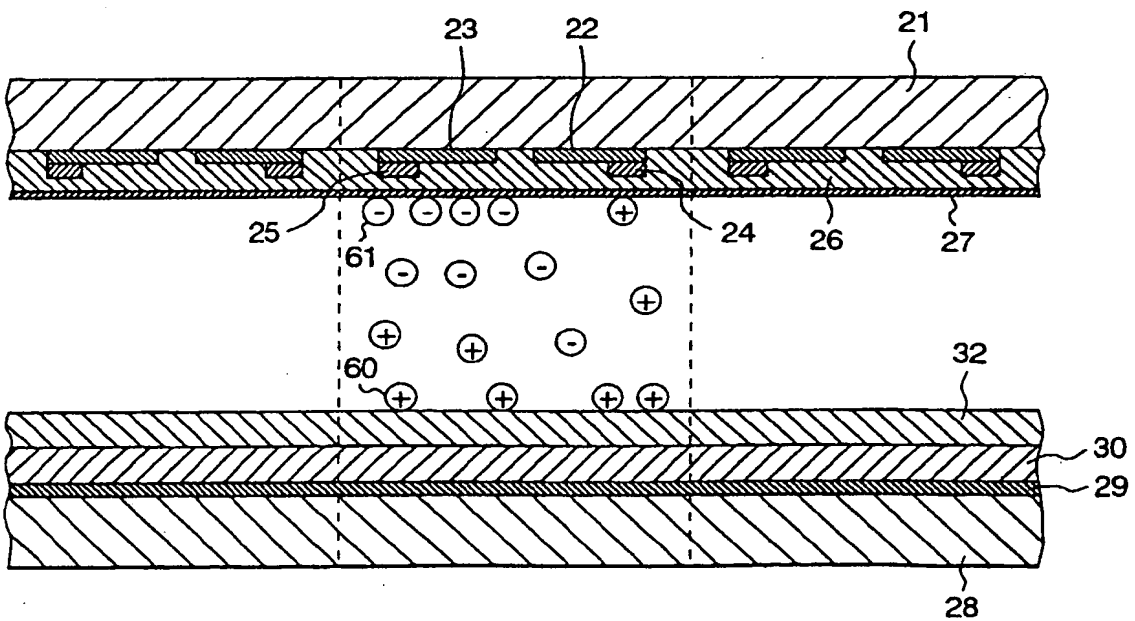


FIG. 10

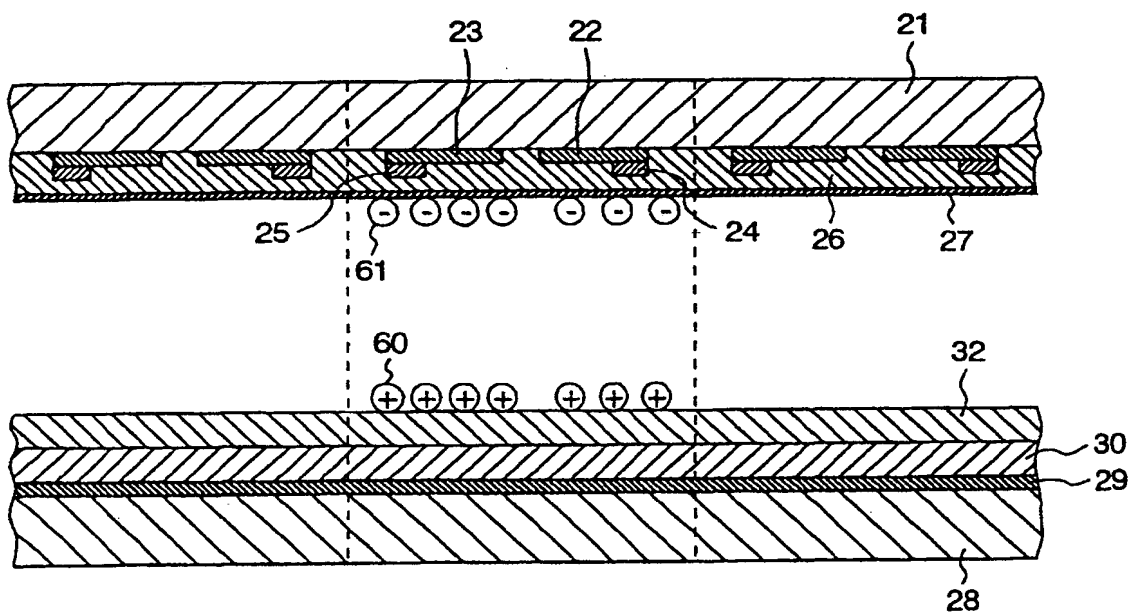


FIG. 11(a)

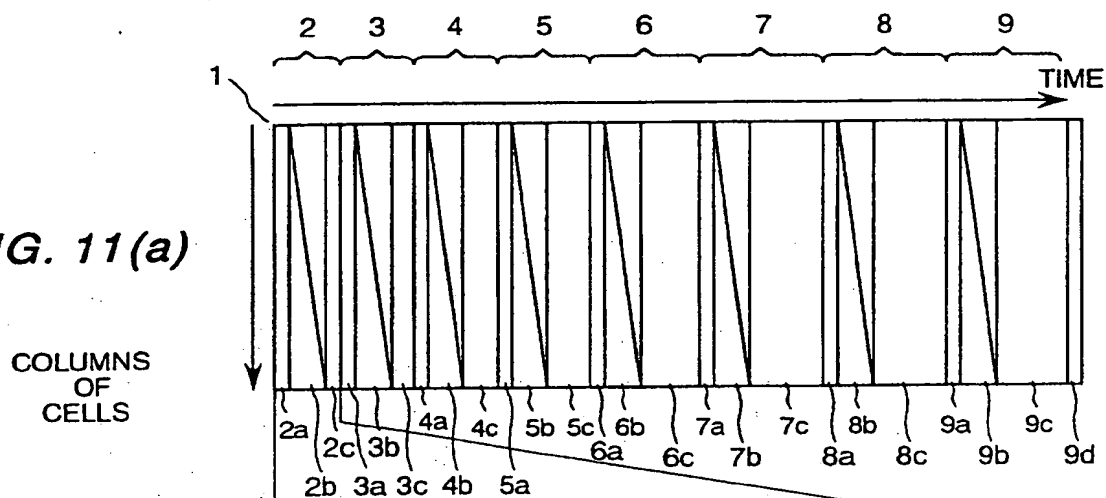


FIG. 11(b)

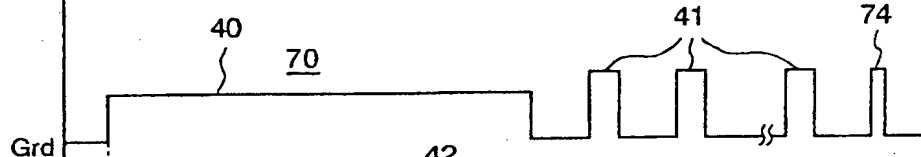


FIG. 11(c)



FIG. 11(d)

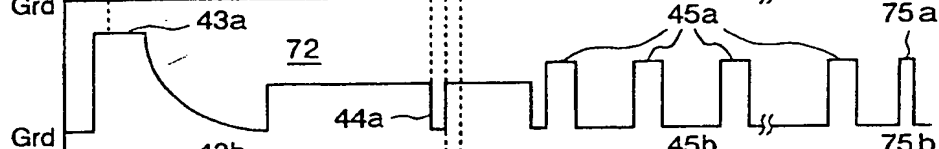


FIG. 11(e)

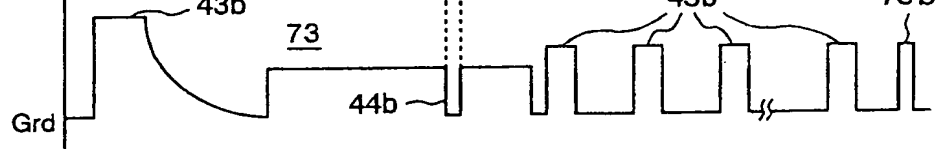


FIG. 12(a)

COLUMNS
OF
CELLS

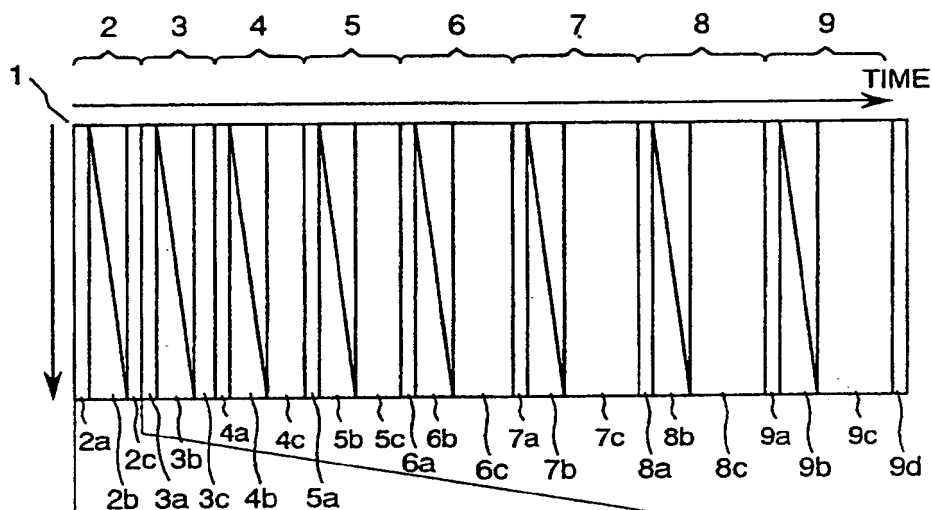


FIG. 12(b)

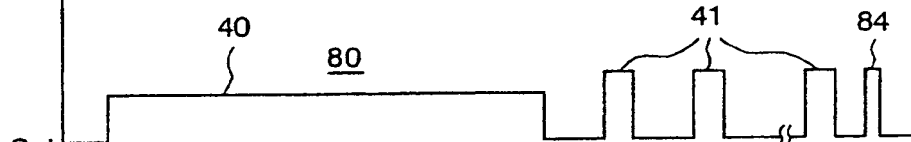


FIG. 12(c)

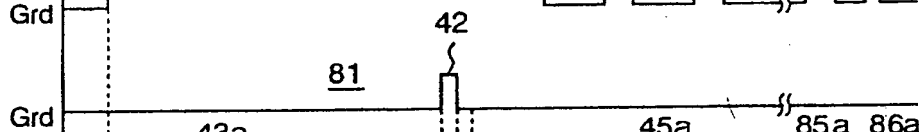


FIG. 12(d)

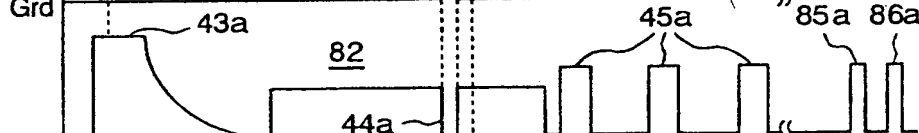


FIG. 12(e)

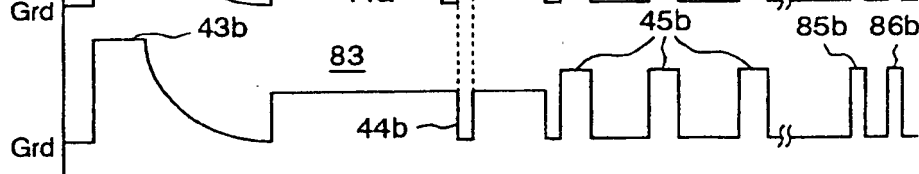


FIG. 13(a)

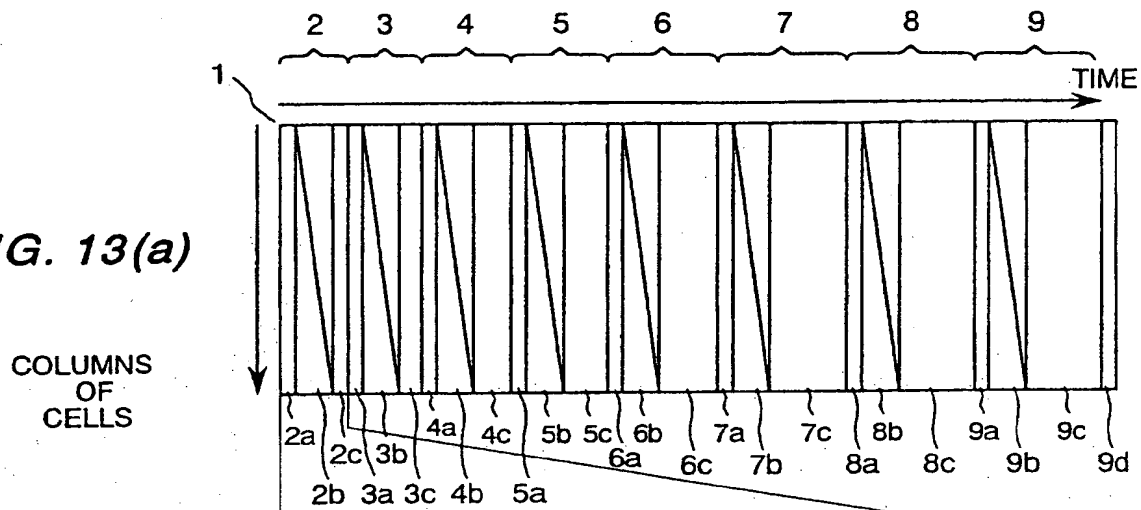


FIG. 13(b)

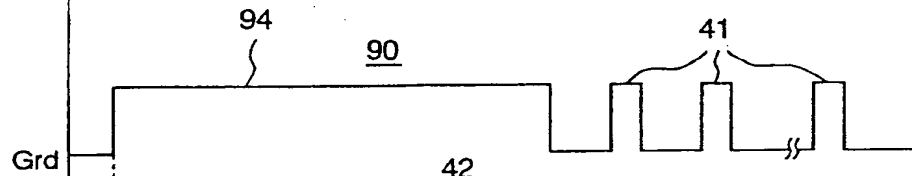


FIG. 13(c)

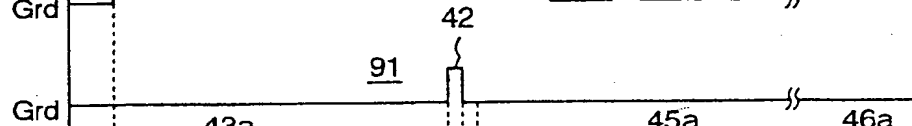


FIG. 13(d)

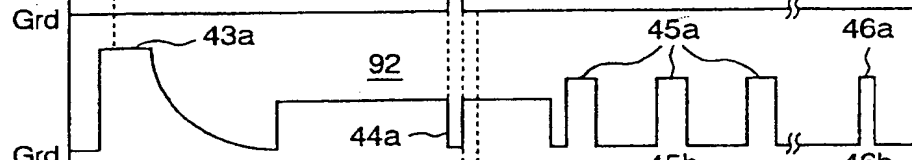
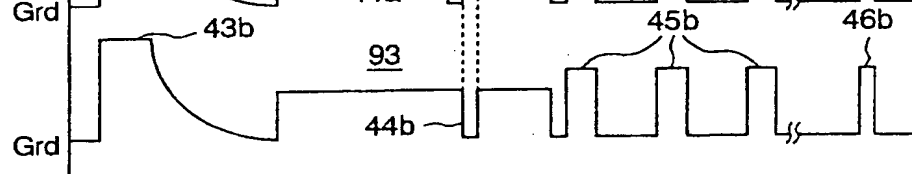
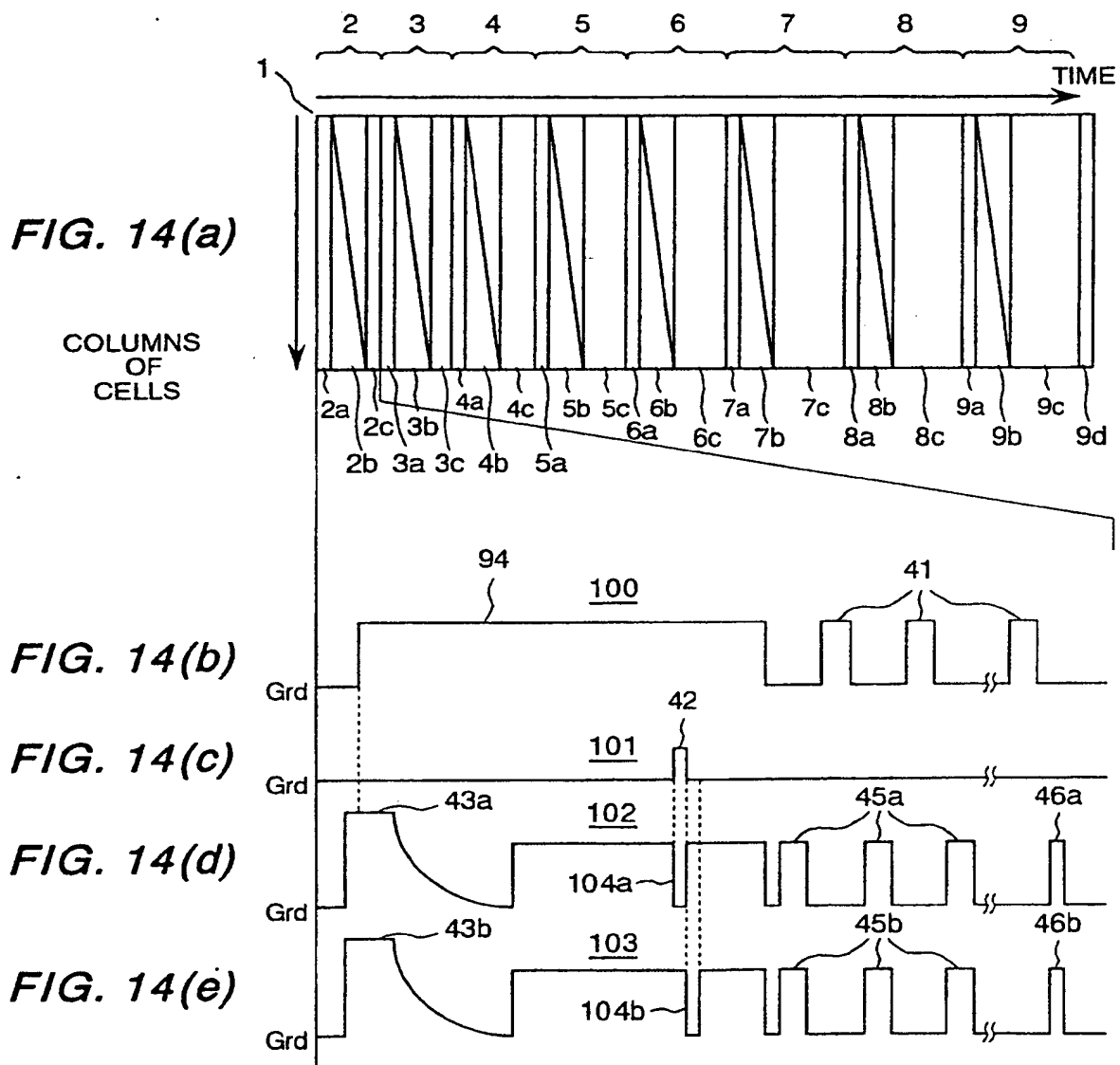
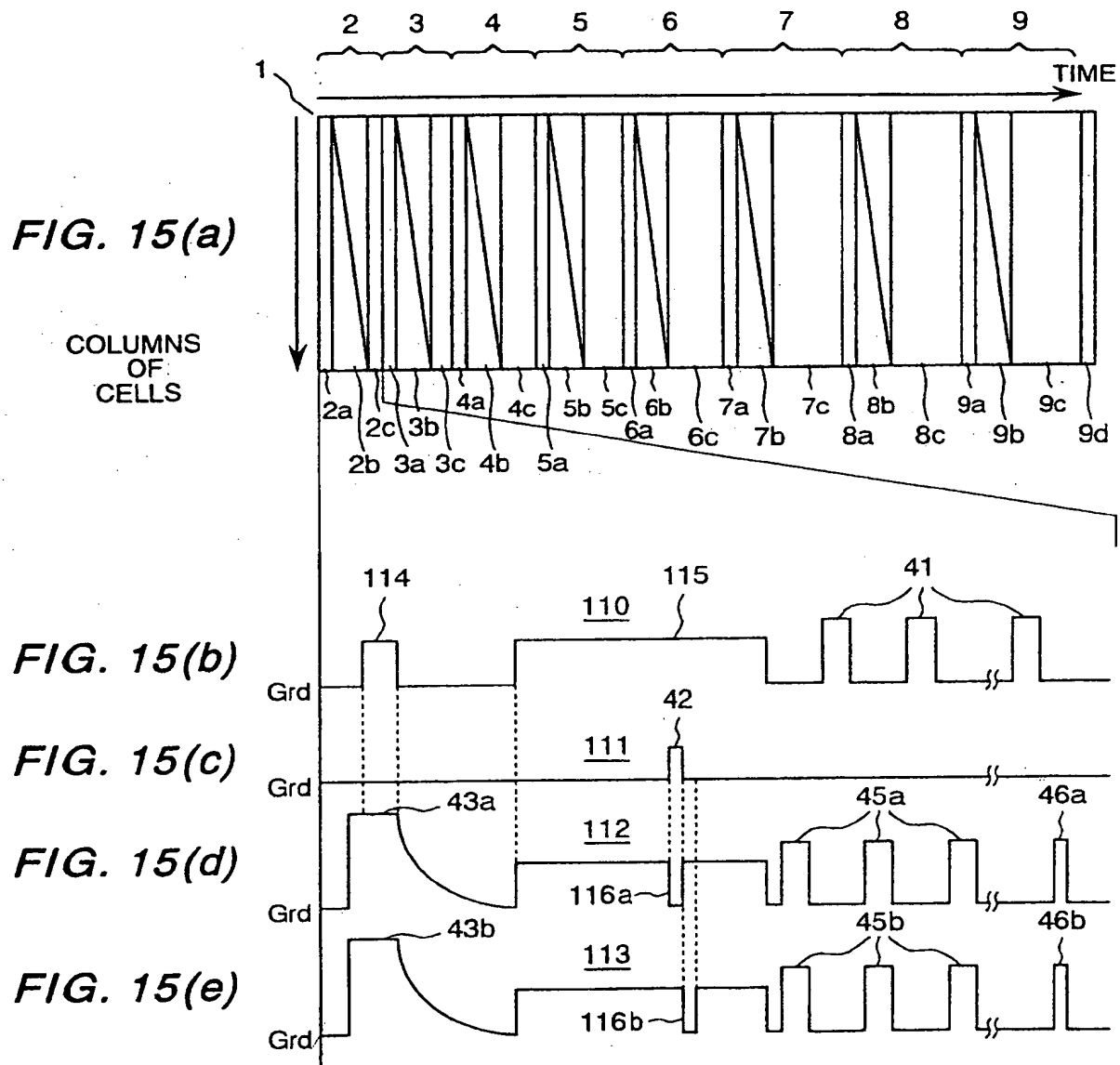


FIG. 13(e)







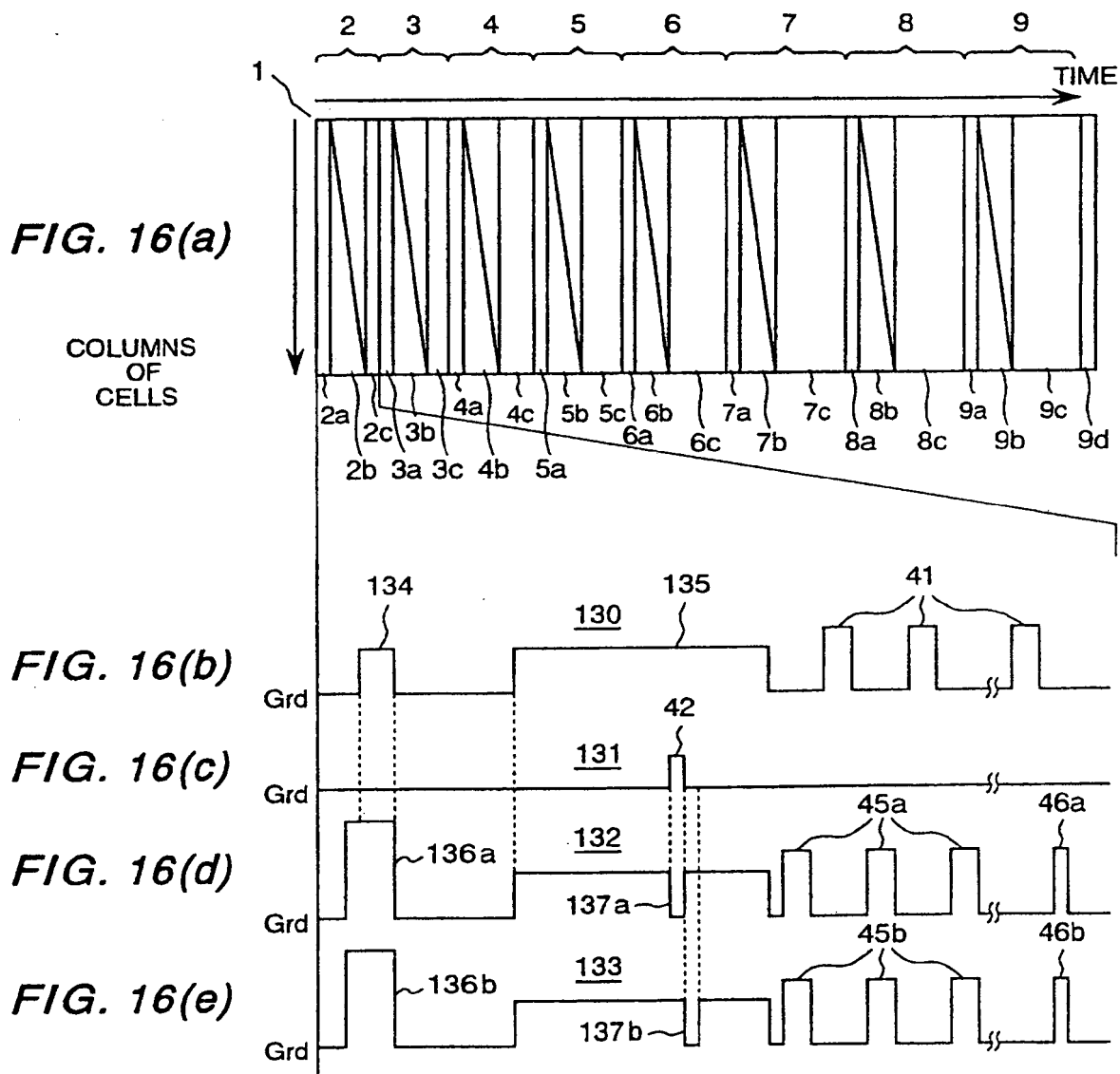


FIG. 17(a)

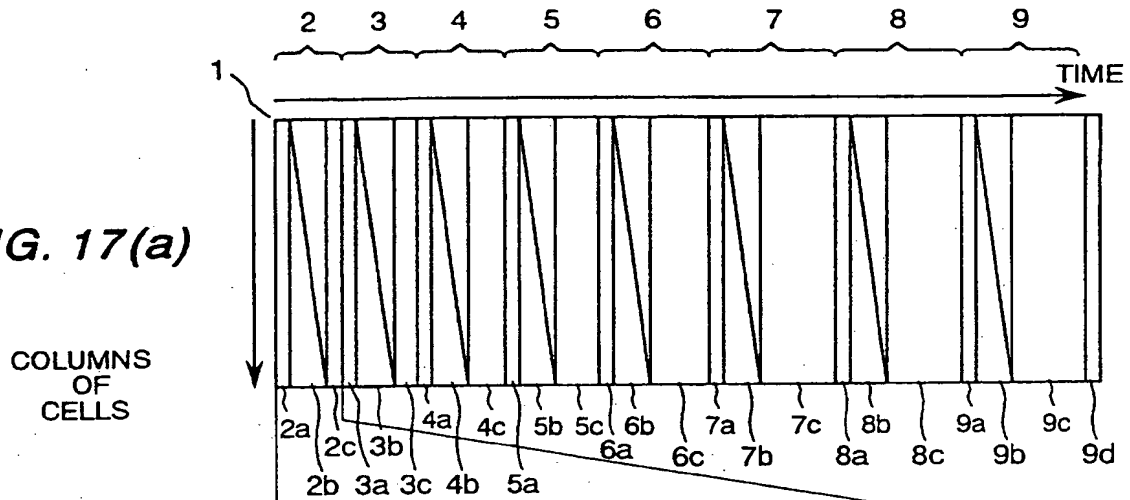


FIG. 17(b)

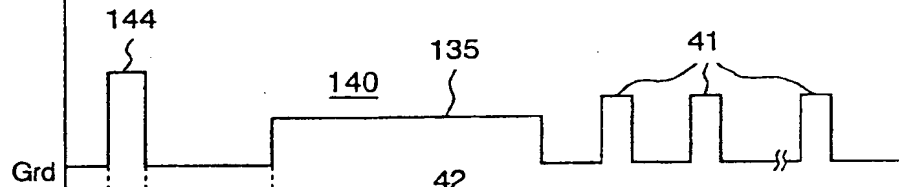


FIG. 17(c)



FIG. 17(d)

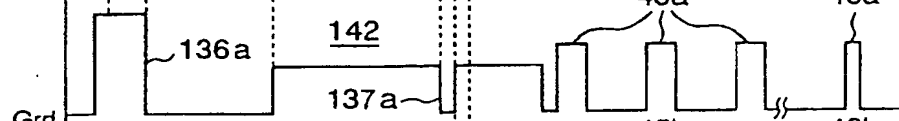


FIG. 17(e)

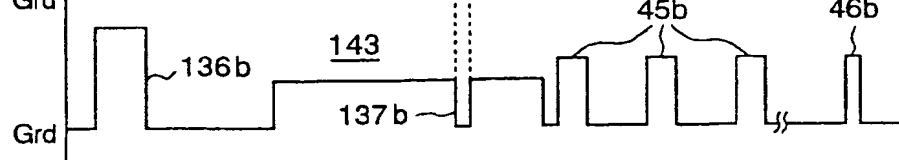


FIG. 18(a)

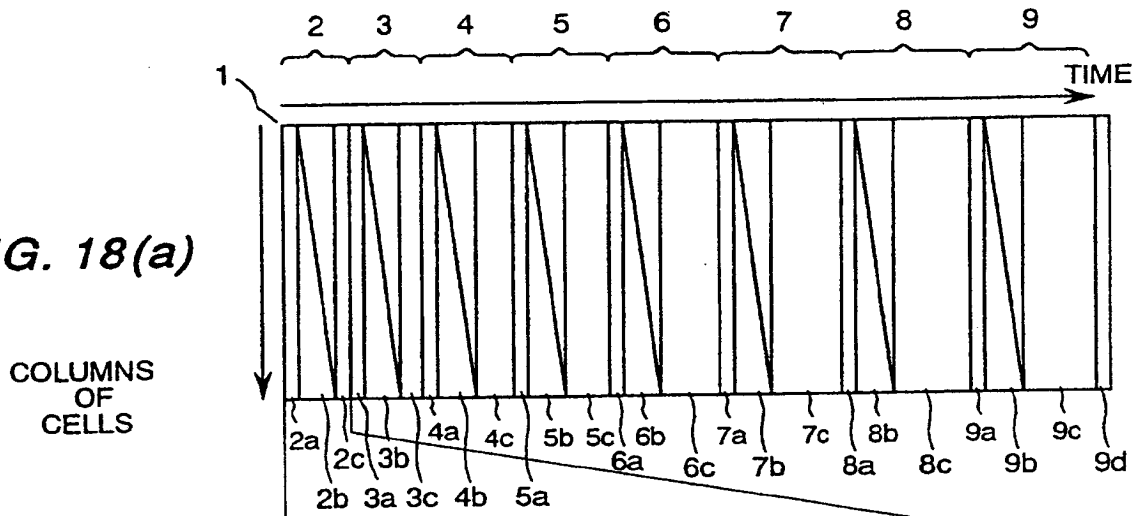


FIG. 18(b)

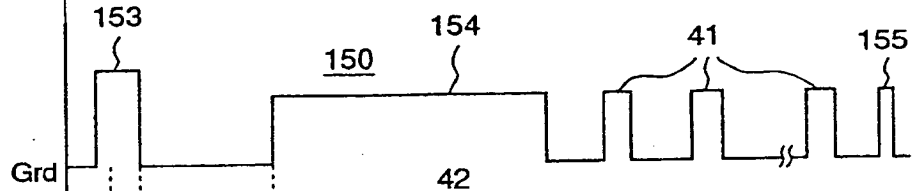


FIG. 18(c)



FIG. 18(d)

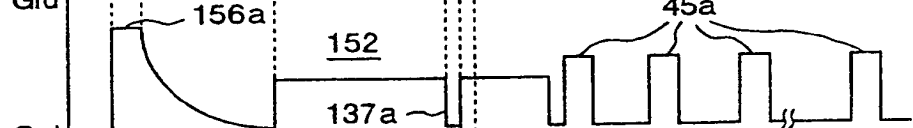


FIG. 18(e)

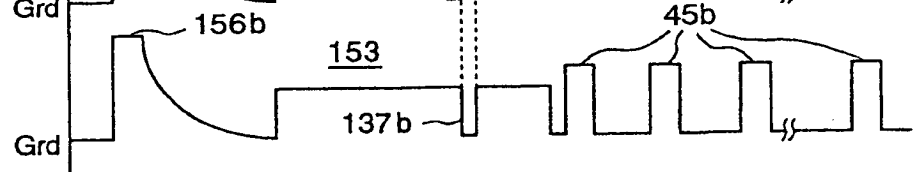


FIG. 19

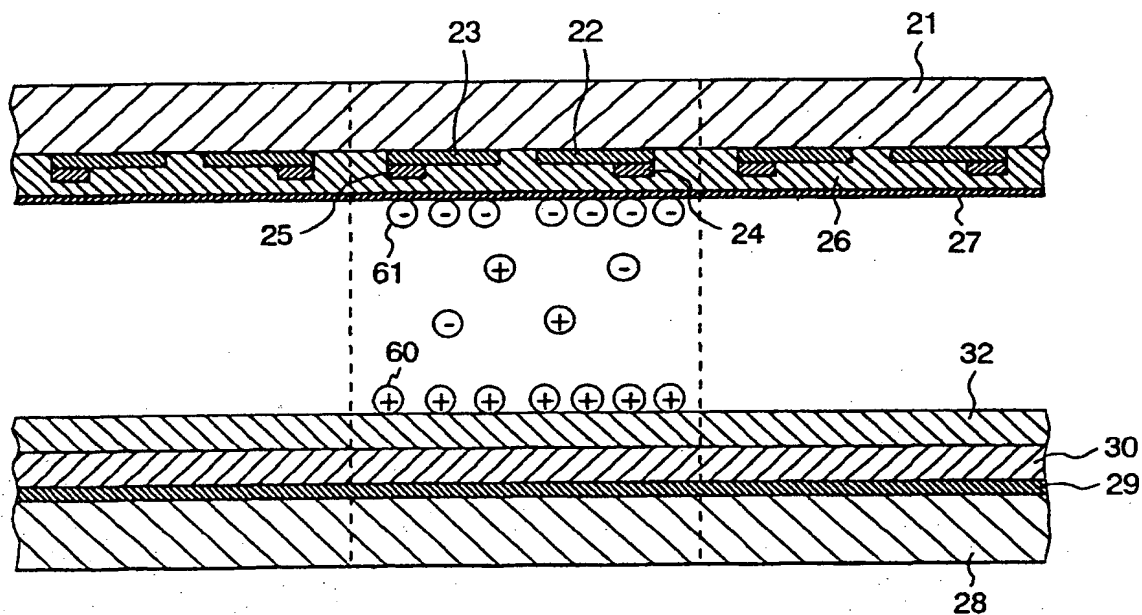


FIG. 20

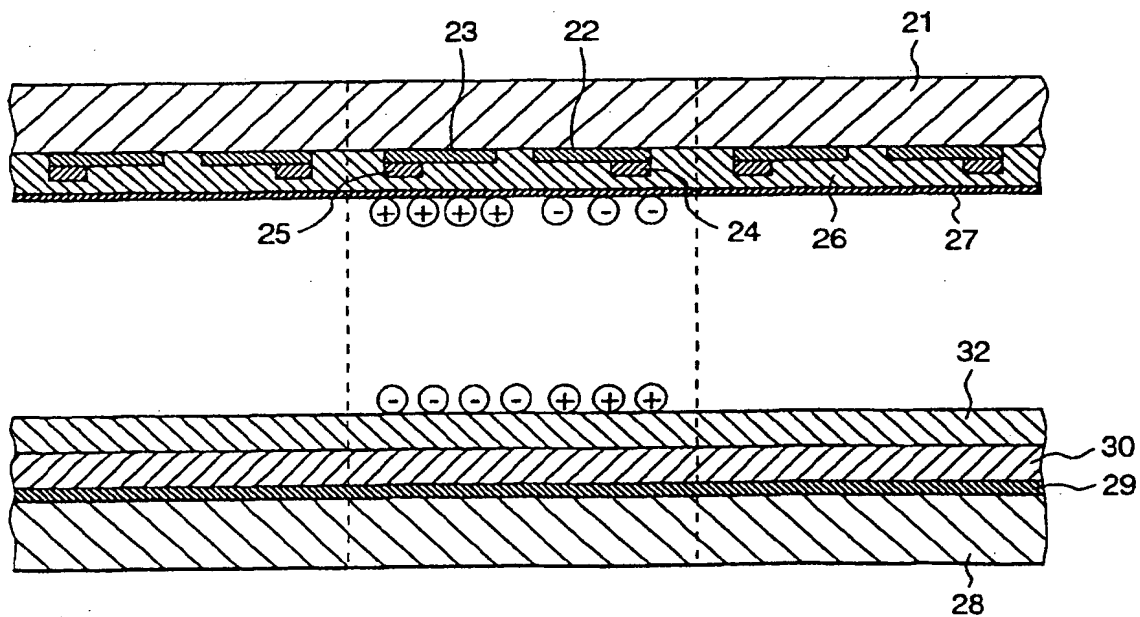


FIG. 21

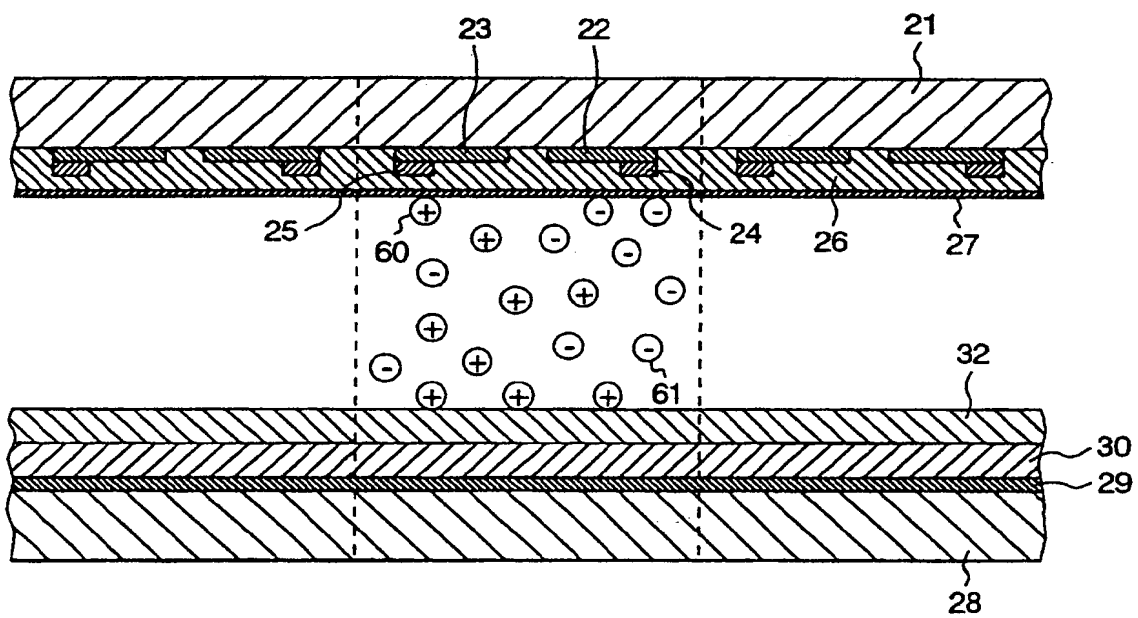


FIG. 22

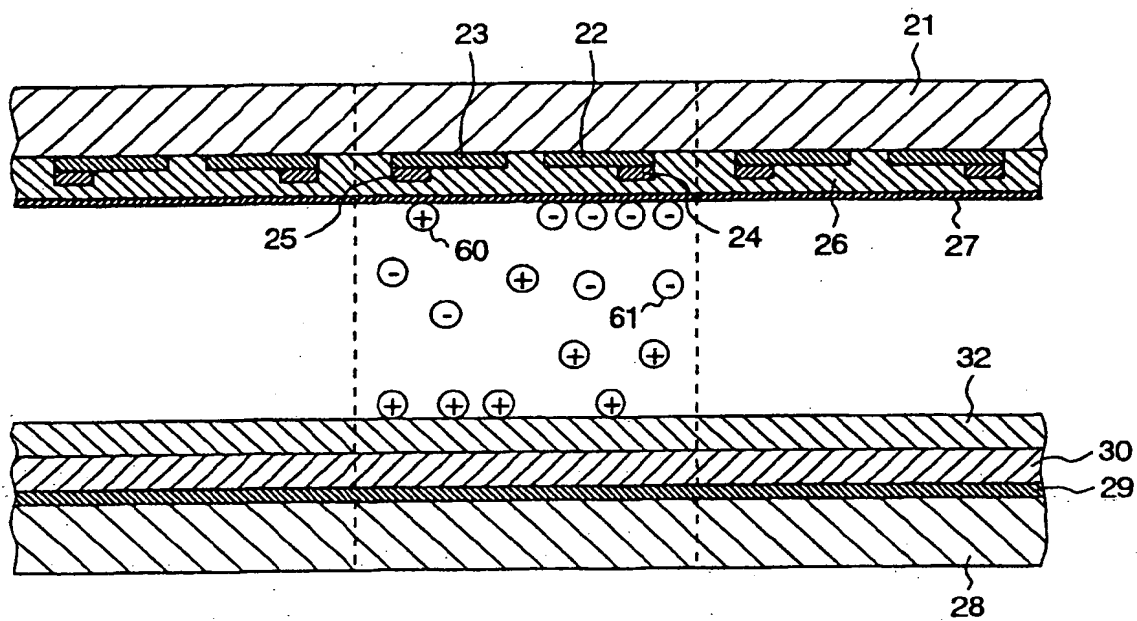


FIG. 23

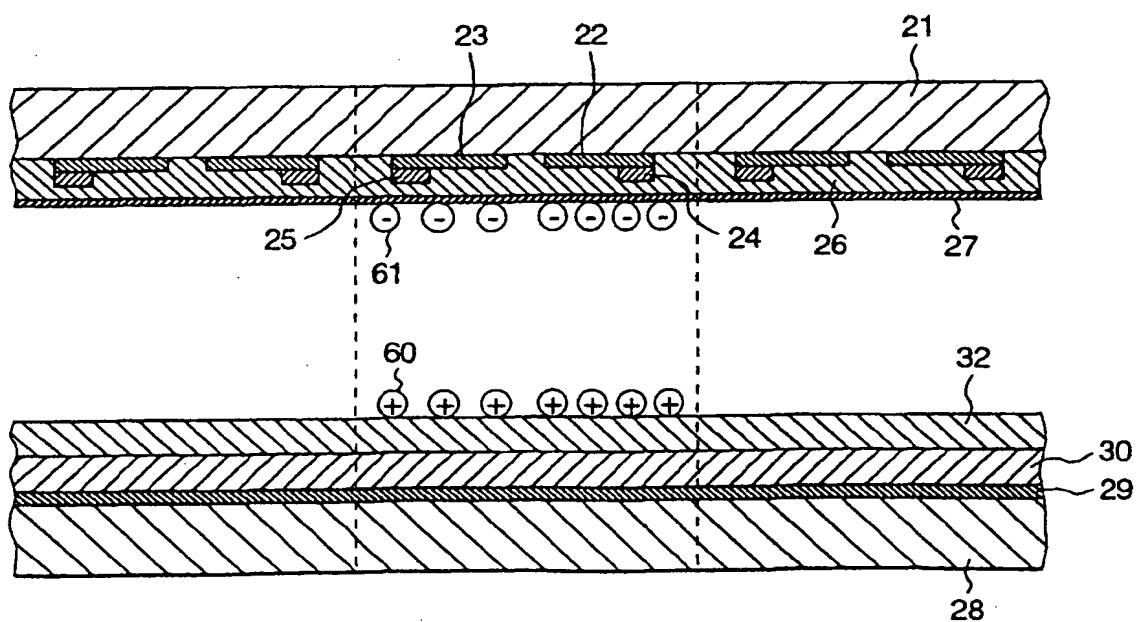


FIG. 24(a)

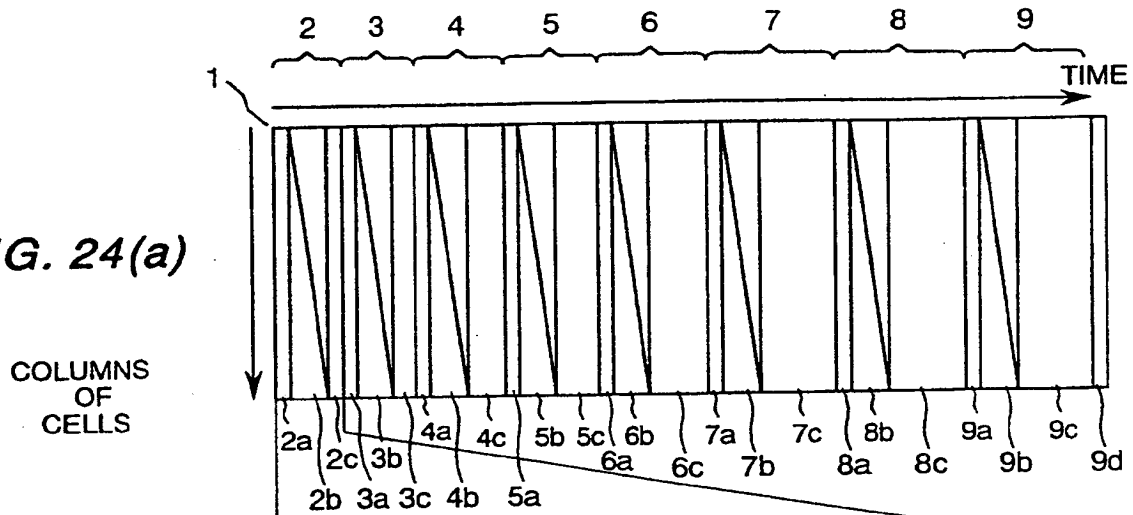


FIG. 24(b)

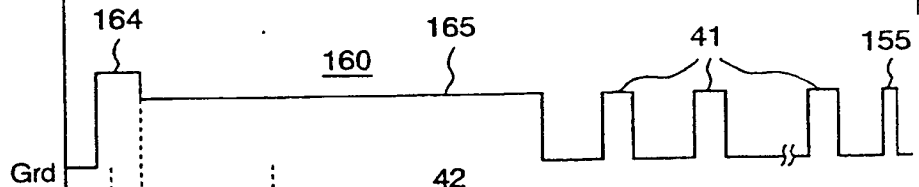


FIG. 24(c)

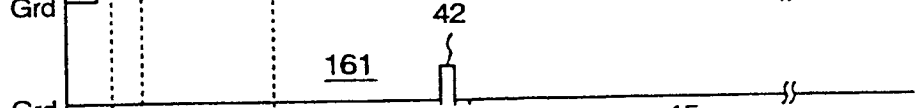


FIG. 24(d)

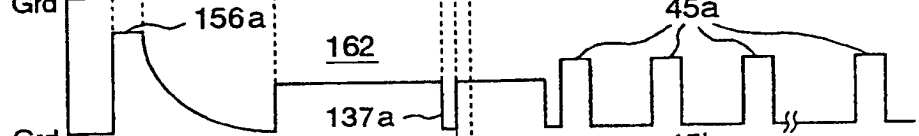


FIG. 24(e)

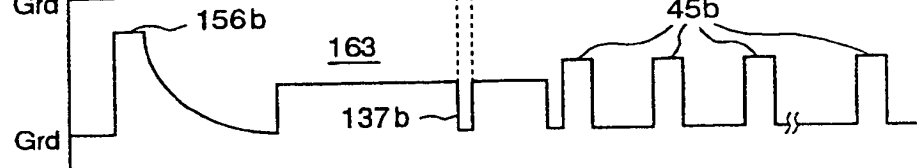


FIG. 25(a)

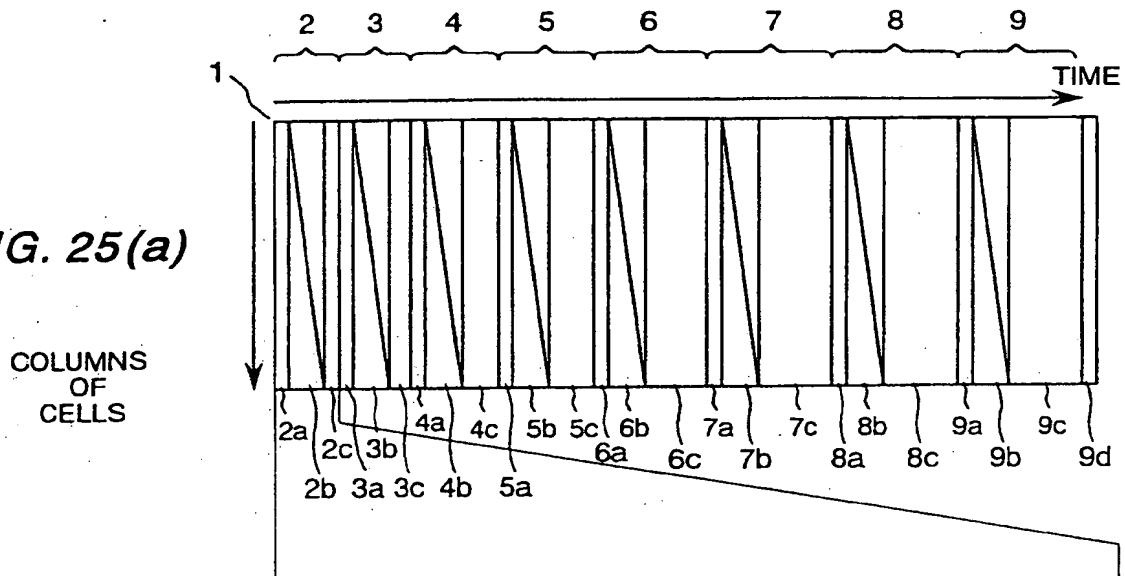


FIG. 25(b)

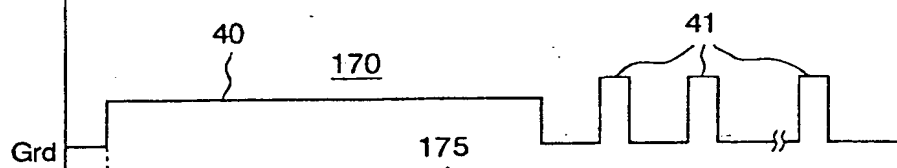


FIG. 25(c)



FIG. 25(d)



FIG. 25(e)



FIG. 26(a)

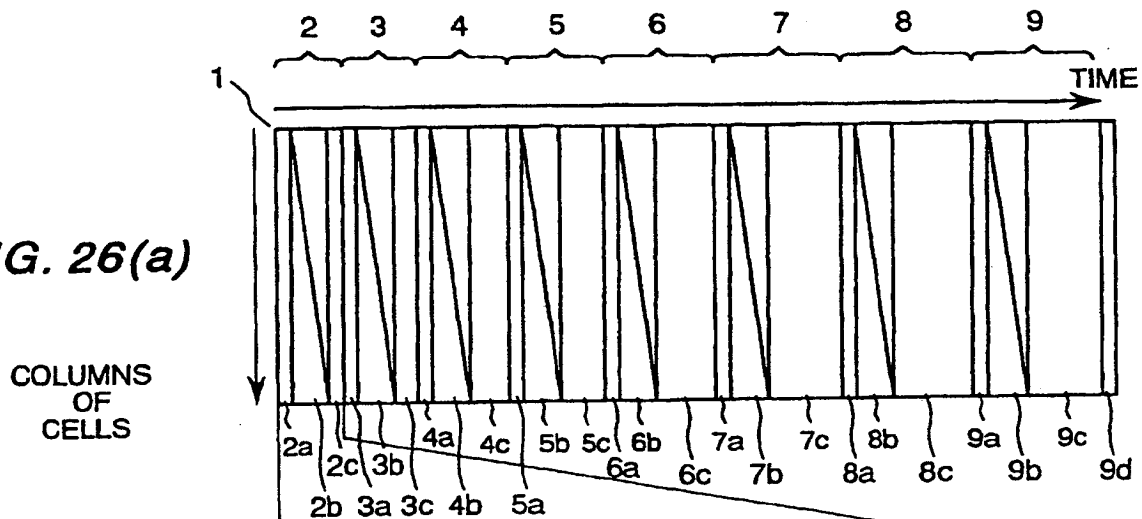


FIG. 26(b)

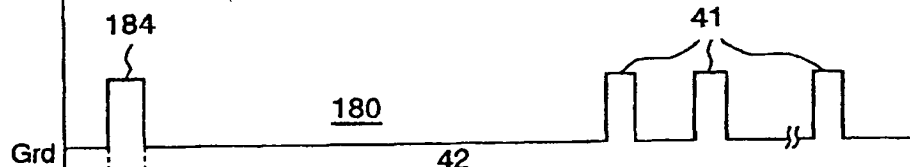


FIG. 26(c)

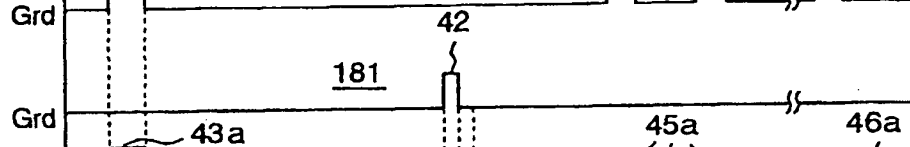


FIG. 26(d)

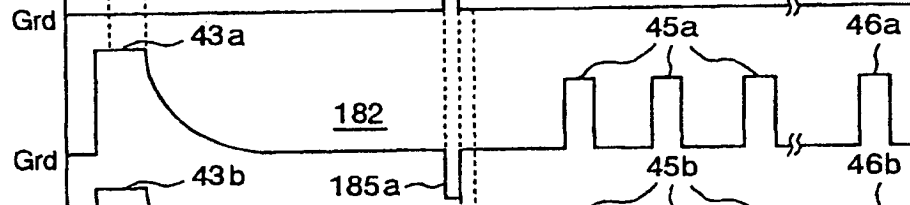


FIG. 26(e)

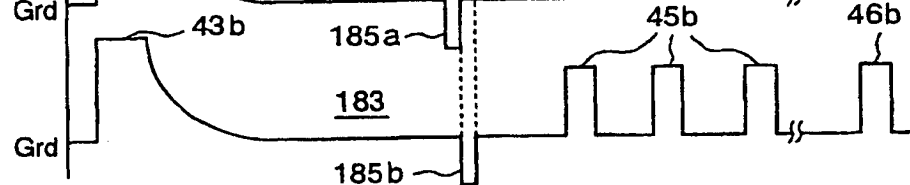


FIG. 27(a)

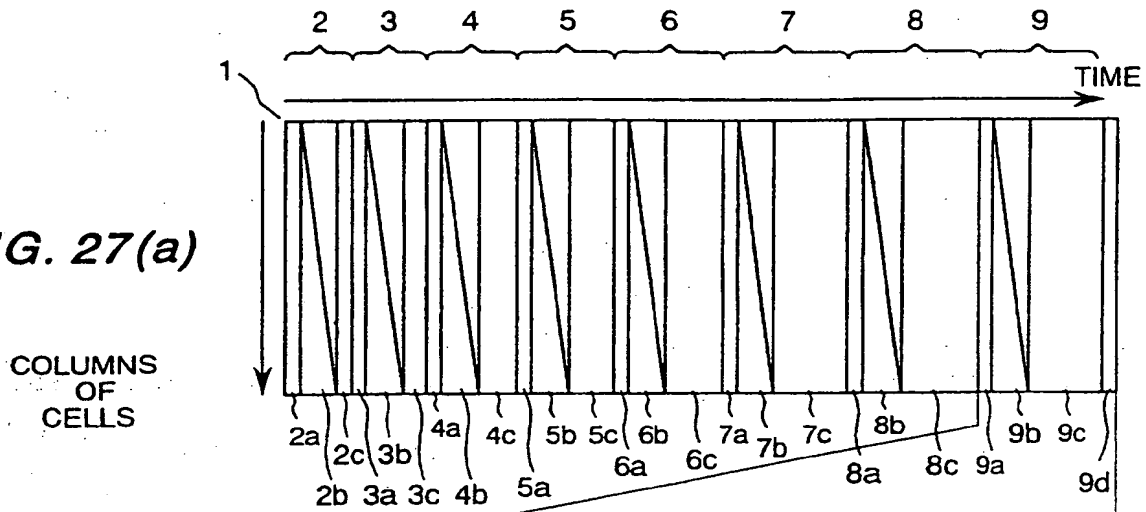


FIG. 27(b)

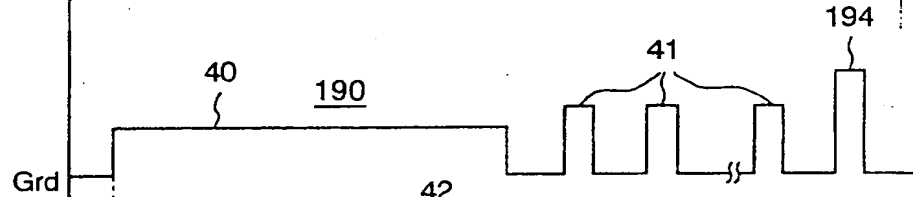


FIG. 27(c)

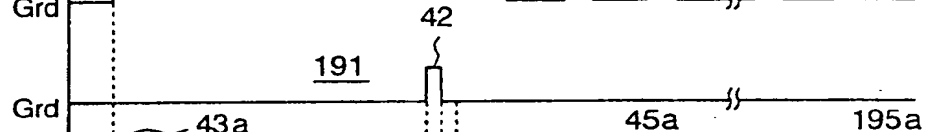


FIG. 27(d)

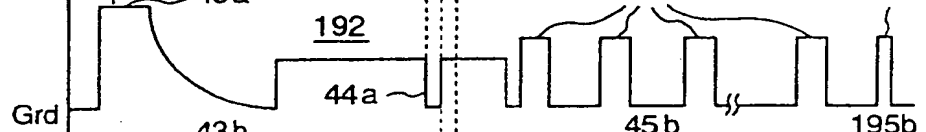
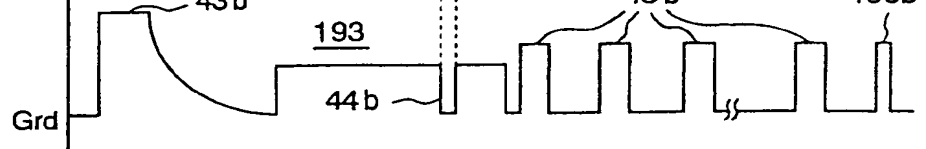


FIG. 27(e)



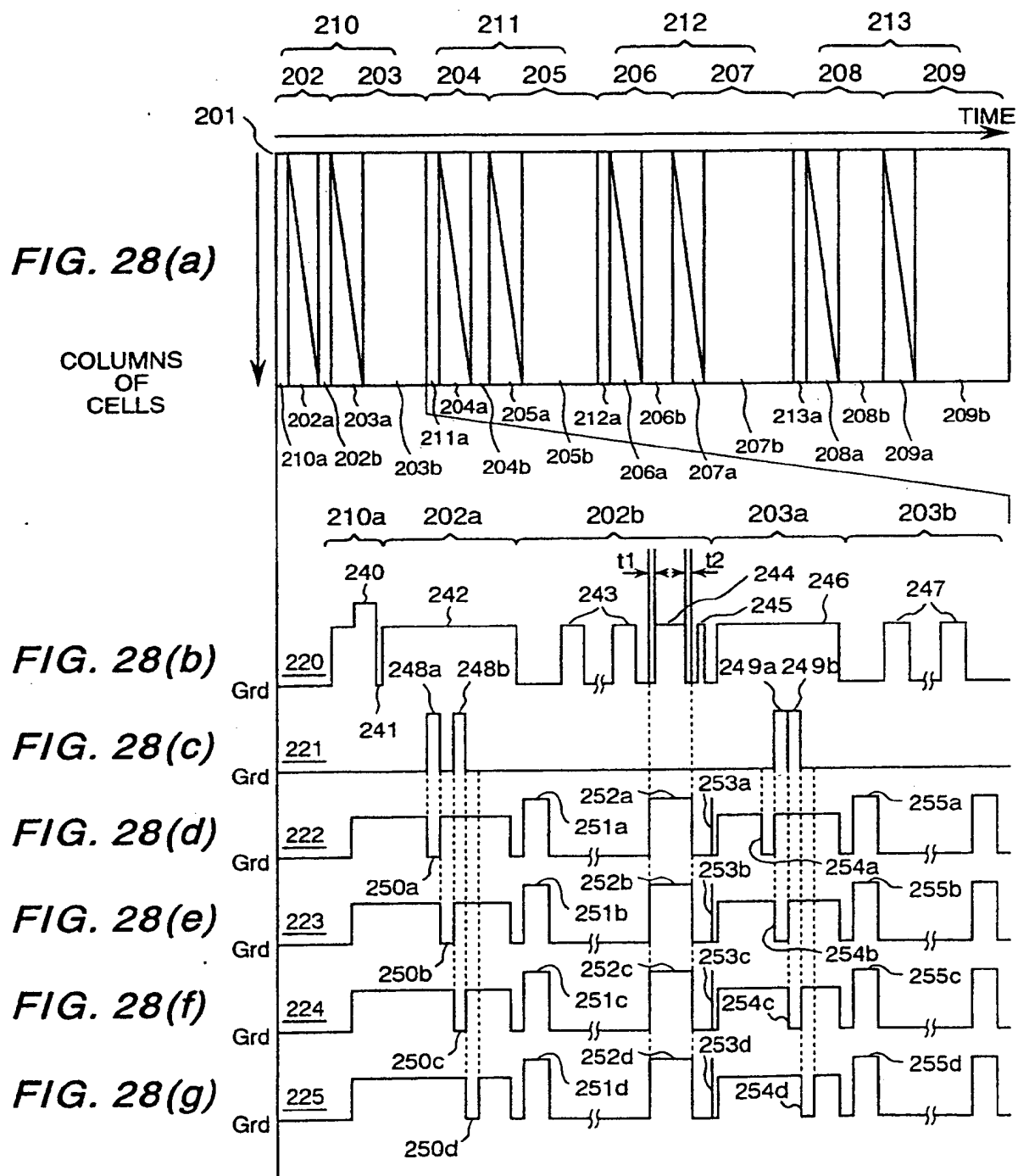


FIG. 29

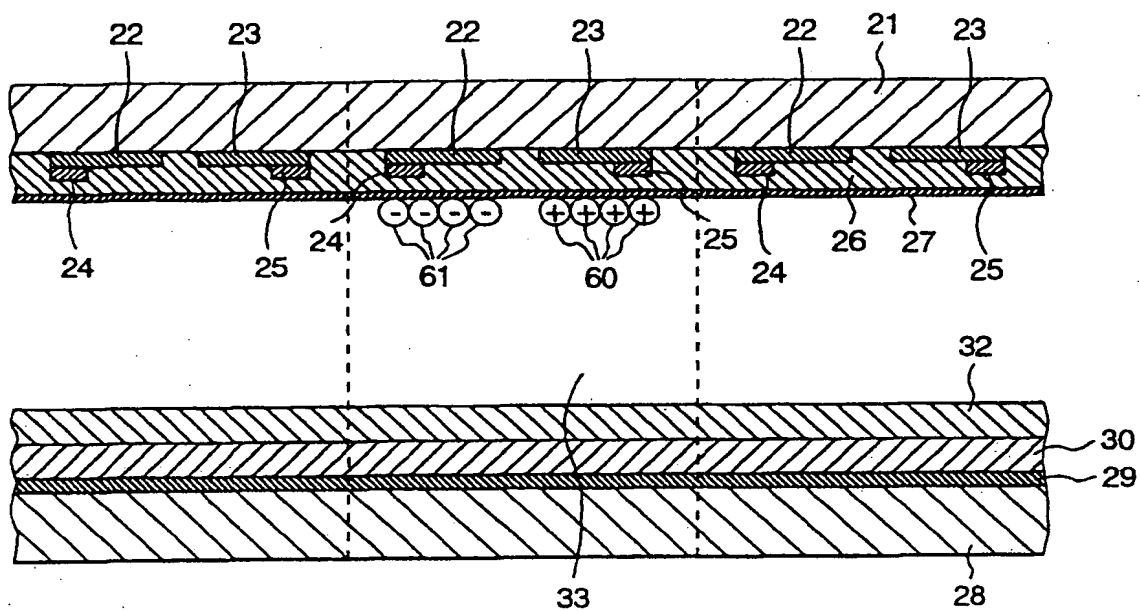


FIG. 30

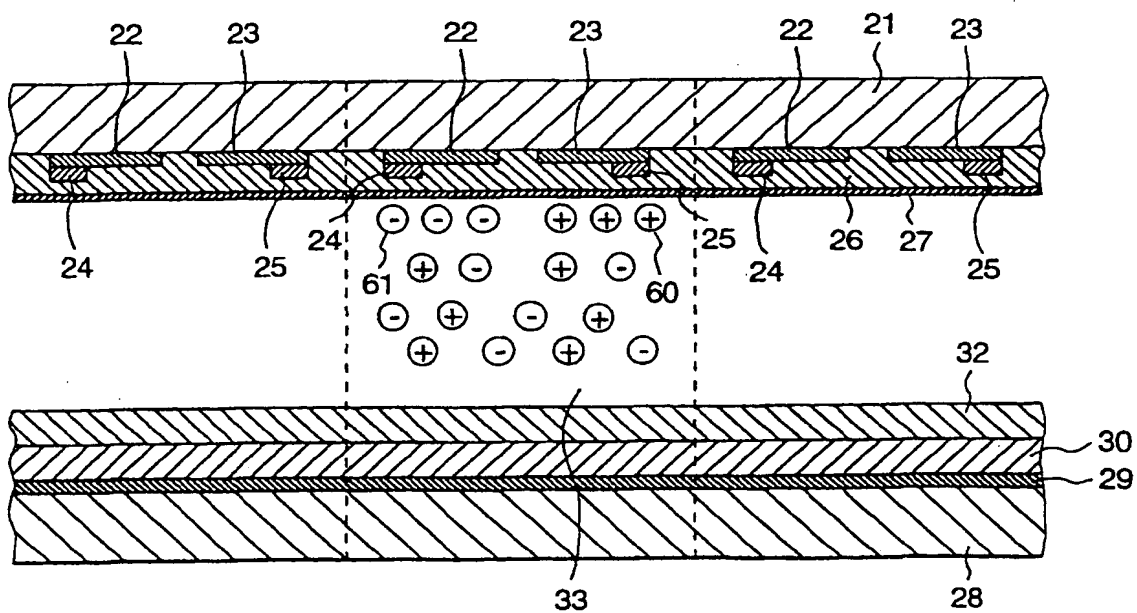


FIG. 31

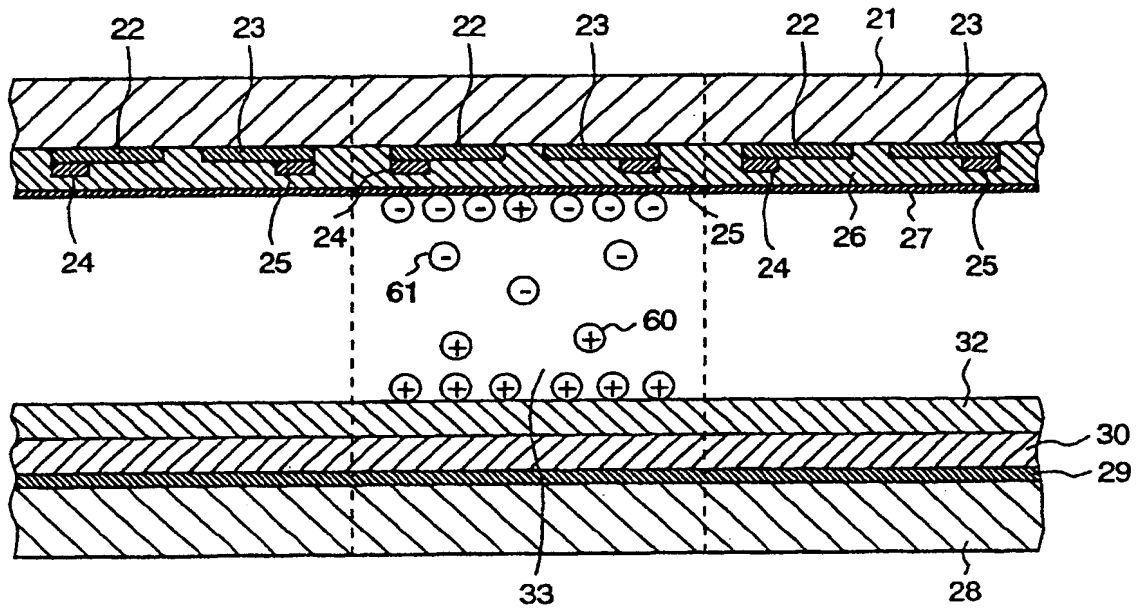


FIG. 32

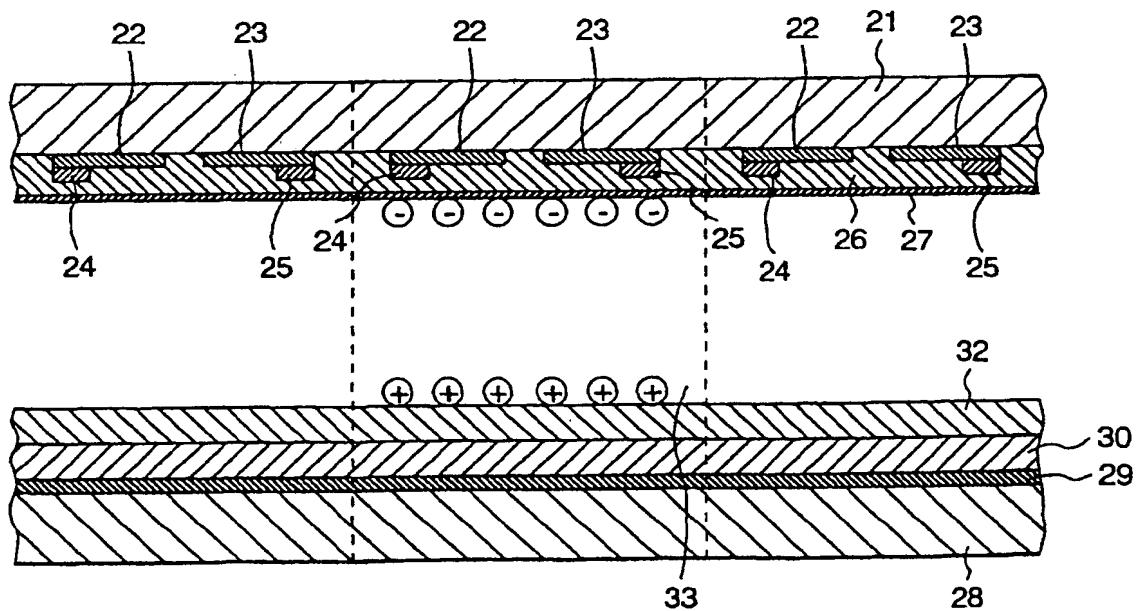


FIG. 33

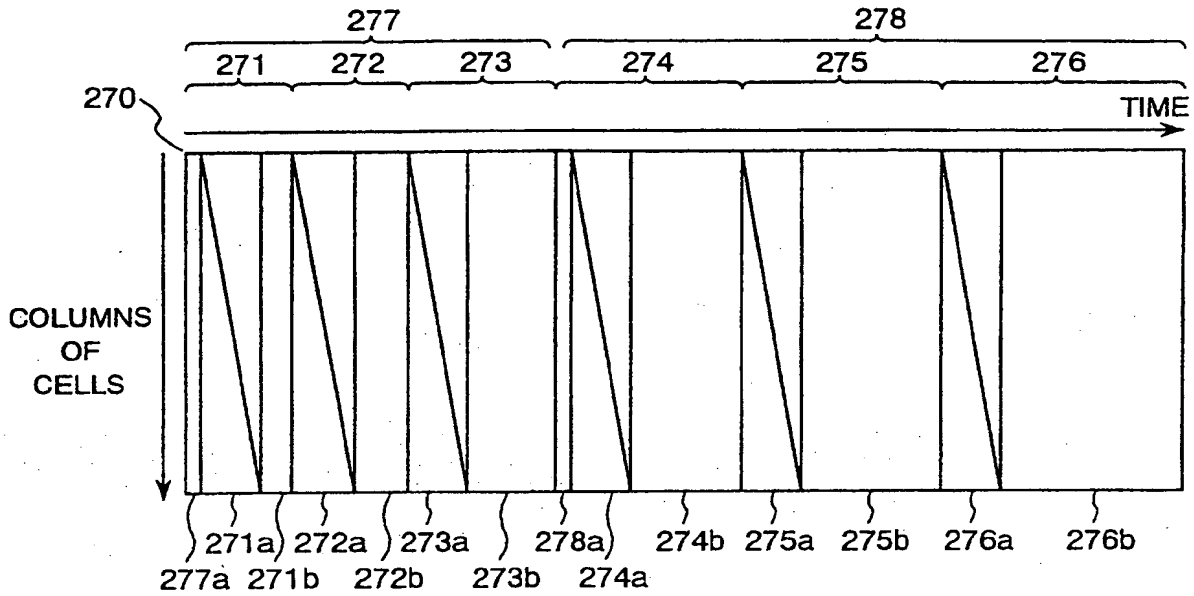


FIG. 34

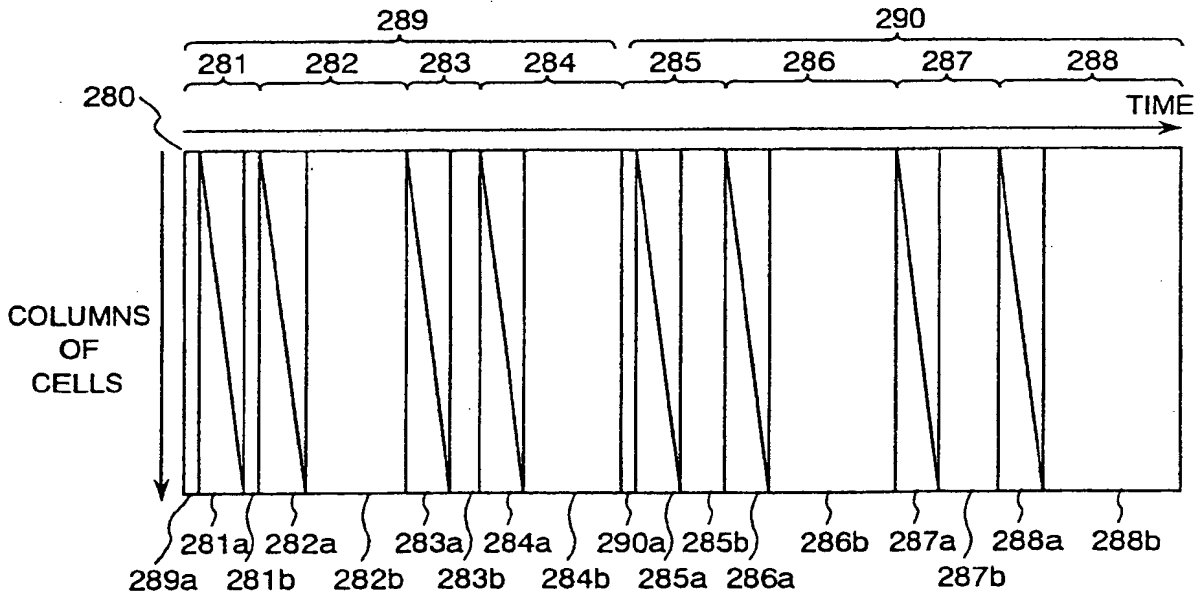
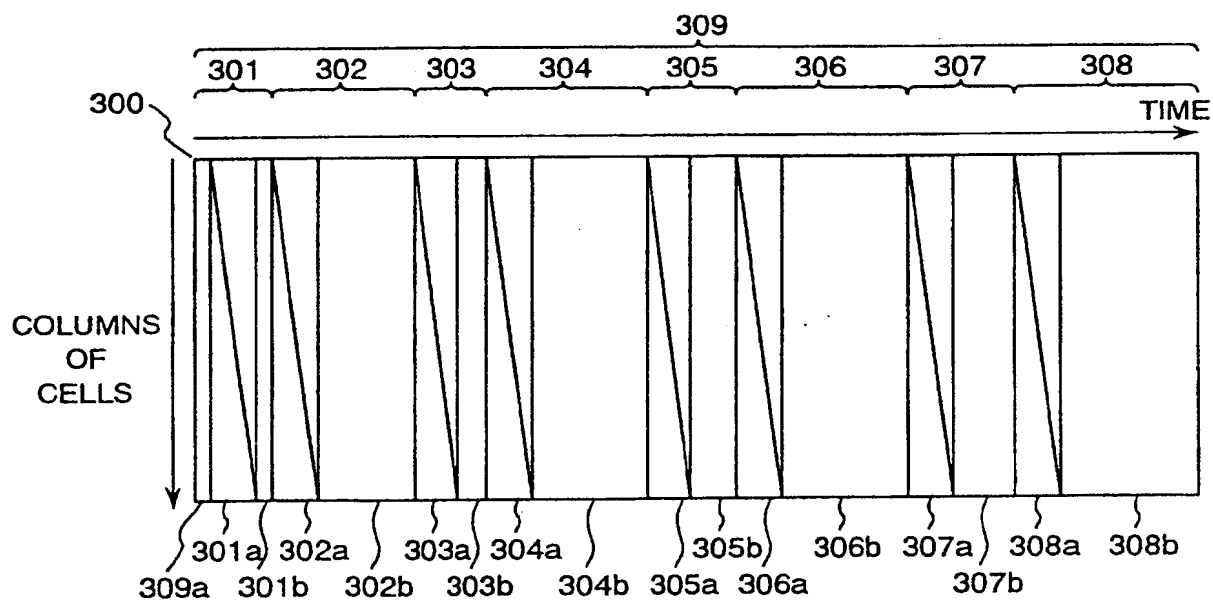
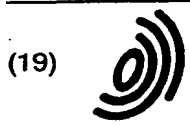


FIG. 35





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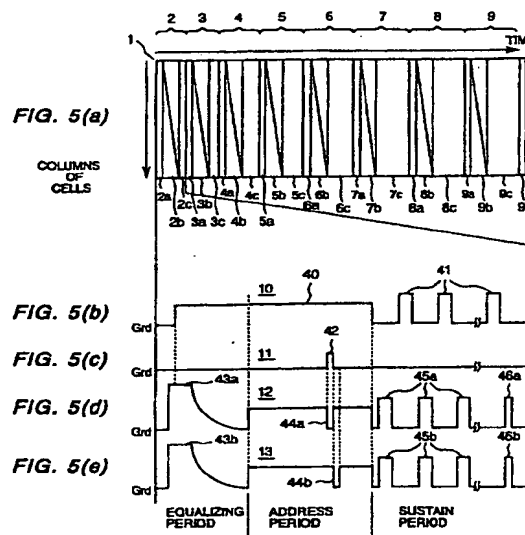
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(54) Plasma display, driving apparatus of plasma display panel and driving system thereof

(57) A plasma display panel driving system having a common X electrode 22 arranged on a front glass substrate 21 driven in common, a independent Y electrode 23 arranged parallel to the common X electrode 22 on the front glass substrate 21 driven independently, an address A electrode arranged perpendicular to the common X electrode and the independent Y electrode on the back glass substrate driven independently, and means for performing at least one electric discharge for equalizing of electric charge particle in a cell in which another electric charge particle is caused beforehand, thereby improving contrast.

Erasing and polarization of electric charge particles are performed by the fine line erasing pulse 46a after sustaining period 2c, and a equalizing pulse having a high voltage is supplied to the independent Y electrode to which the last fine line erasing pulse 46a or 86a is supplied, and a regulating pulse 40 is supplied to the common X electrode after supplying of the equalizing pulse.

Further, a field bloc having plurality of sub-fields are provided, and a full writing electric discharge and a fine line erasing electric discharge are performed in the first sub-field of each field block for reducing the number of electric discharging.



EP 0 836 171 A3



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EUROPEAN SEARCH REPORT

Application Number
EP 97 11 7253

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X A	EP 0 657 861 A (FUJITSU LTD.) * abstract * * column 1, line 47 - column 2, line 47 * * column 9, line 41 - column 10, line 34 * * column 11, line 25 - line 30 * * column 15, line 18 - column 19, line 43; figures 8,9 * ---	1-3,26 4-25, 27-34	G09G3/28
A	EP 0 680 067 A (MATSUSHITA ELECTRONICS CORP.) * column 47, line 50 - line 58 * * column 48, line 42 - column 58, line 46; figures 27-36 * -----	1-34	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			G09G
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 27 January 1998	Examiner D.J. O'REILLY
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03.82 (P04C01)

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CLAIMS INCURRING FEES

EP 97117253.1

The present European patent application comprised at the time of filing more than ten claims.

- ☐ All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid.
- namely claims:
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirement of unity of invention and relates to several inventions or groups of inventions.

namely:

See Sheet B.

- ☐ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid.
- namely claims:
- ☒ None of the further search fees has been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims.
- namely claims: 1-34

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**LACK OF UNITY OF INVENTION
SHEET B**

Application Number
EP 97 11 7253

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. Claims: 1-34

Reduce black level luminance by providing cell
initialisation pulses that do not cause light emission.

2. Claims: 35-44

Reduce black level luminance by reducing the number of
subfields with light emitting initialisation pulses.

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